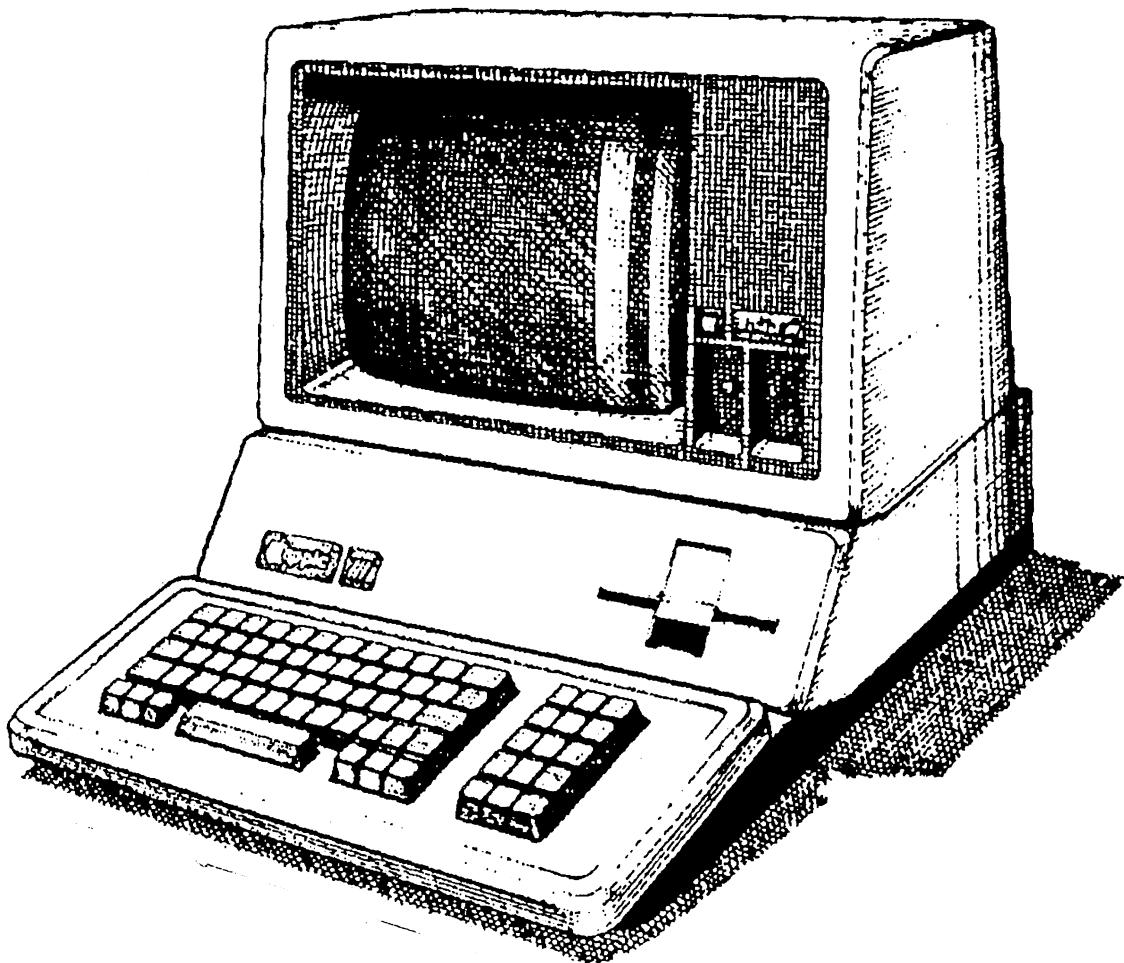




SEE DOC # 193

Apple /// Computer Information



DOCUMENT NAME

APPLE /// BOOT ROM LISTING
(DISKS INCLUDED)

ROM Rev. 1
Jan. 1980

85

Ex Libris David T. Craig

"_43.PICT" 748 KB 2001-09-10 dpi: 600h x 600v pix: 4344h x 5811v

Assembler: Apple III Pascal TLA 6502 Assembler
(converted to TLA format most likely by Scott Stinson)

41 pages

Source Code Listing

for

Apple //

Sara ROM

\$FO00-\$FFFF 4KB

REVISION 1

(See A// patent for Rev 0 ROM)

David T. Craig
736 Edgewater
Wichita, Kansas 67230

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"_44.PICT" 779 KB 2001-09-10 dpi: 600h x 600v pix: 3929h x 6107v

Source Code Listing

for

Apple ////

ROM - Disk I/O

David T. Craig
736 Edgewater
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HD:Apple //:ROM - Disk I/O

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HD:Apple ///:ROM - Disk I/O

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F000| ; SIGNIFICANT BYTES      *
F000| ; OF DNIBL TABLE.      *
F000| ;*****
F000| ;*****
F000| ;-----SEEK----- *
F000| ;*****
F000| ;*****
F000| 0095  TRKCNT    .EQU    COUNT      ; HALFTRACKS MOVED COUNT.
F000| 009D  PRIOR     .EQU    IBSLOT+1C
F000| 009E  TRKN      .EQU    IBSLOT+1D
F000| ;
F000| ;*****
F000| ;-----MSWAIT---- *
F000| ;*****
F000| ;*****
F000| 0099  MONTIMEL   .EQU    CSSTV+2 ; MOTOR-ON TIME
F000| 009A  MONTIMEH   .EQU    MONTIMEL+1 ; COUNTERS.
F000| ;
F000| ;*****
F000| ; DEVICE ADDRESS      *
F000| ; ASSIGNMENTS        *
F000| ;*****
F000| C080  PHASEOFF   .EQU    0C080      ; STEPPER PHASE OFF.
F000| C081  PHASEON    .EQU    0C081      ; STEPPER PHASE ON.
F000| C08C  Q6L        .EQU    0C08C      ; Q6L,Q6L=READ
F000| C08D  Q6H        .EQU    0C08D      ; Q7L,Q6H=SENSE WPROT
F000| C08E  Q7L        .EQU    0C08E      ; Q7H,Q6L=WRITE
F000| C08F  Q7H        .EQU    0C08F      ; Q7H,Q6H=WRITE STORE
F000| FFEF  INTERRUPT  .EQU    0FFEF
F000| FFDF  ENVIRON   .EQU    0FFDF
F000| 0080  ONEMEG    .EQU    80
F000| 007F  TWOMEGR   .EQU    7F
F000| ;
F000| ;*****
F000| ; EQUATES FOR RWTS AND BLOCK
F000| ;
F000| ;*****
F000| C088  MOTOROFF   .EQU    0C088
F000| C089  MOTORON    .EQU    0C089
F000| C08A  DRVOEN    .EQU    0C08A
F000| C08B  DRV1EN    .EQU    0C08B
F000| C081  PHASON    .EQU    0C081
F000| C080  PHSOFF    .EQU    0C080
F000| 0097  TEMP       .EQU    CSSTV      ; PUT ADDRESS INFO HERE
F000| 0097  CSUM1     .EQU    TEMP
F000| 0098  SECT       .EQU    CSUM1+1
F000| 0099  TRACK      .EQU    SECT+1
F000| 0099  TRKN1     .EQU    TRACK
F000| 009A  VOLUME     .EQU    TRACK+1
F000| 0083  IBRERR    .EQU    HRDERRS+3
F000| 0082  IBDERR    .EQU    HRDERRS+2
F000| 0081  IBWPER    .EQU    HRDERRS+1
F000| 0080  IBNODRV   .EQU    HRDERRS
F000| ;
F000| ;*****
F000| ; READ WRITE A      *
F000| ; TRACK AND SECTOR  *
F000| ;
F000| ;*****
F000| A0 01  REGRWTS  LDY     #01      ; RETRY COUNT
F000| A6 81  LDX     IBSLOT   ; GET SLOT # FOR THIS OPERATION
F000| 84 94  STY     SEEKCNT ; ONLY ONE RECALIBRATE PER CALL
F000| A9 05  LDA     #005
F000| 85 8F  STA     08F      ; DETERMINE INTERRUPT STATUS
F000| 08  PLA
F000| 68  ROR     A
F000| 6A  ROR     A      ; GET INTERRUPT FLAG INTO BIT 7
F000| 6A  ROR     A
F000| 6A  ROR     A
F000| 85 8B  STA     IMASK
F000| AD DFFF  LDA     ENVIRON ; PRESERVE ENVIRONMENT
F000| 85 9F  STA     ENVTEMP
F000| 20 2BF1  JSR     CHKDRV ; SET ZERO FLAG IF MOTOR STOPPED
F000| 08  PHP
F000| A5 85  LDA     IBEUFFP ; SAVE TEST RESULTS
F000| 85 9B  STA     BUF      ; MOVE OUT POINTER TO BUFFER INTO ZPAGE

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"47.PICT" 554 KB 2001-09-10 dpi: 600h x 600v pix: 4687h x 6225v

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F0AE| C4 8C          CPY    CURTRK
F0B0| F00E          BEQ    RTTRK      ; IF SO, GOOD
F0B2|               ; RECALIBRATING FROM THIS TRACK
F0B2|               ;
F0B2| A5 8C          LDA    CURTRK      ; PRESERVE DESTINATION TRACK
F0B4| 48             PHA
F0B5| 98             TYA
F0B6| 0A             ASL    A
F0B7| 20 25F1        JSR    SETTRK
F0BA| 68             PLA
F0BB| 20 04F1        JSR    MYSEEK
F0BE| 90CA          BCC    TRYADR2
F0C0| A5 9A          RTTRK  LDA    VOLUME      ; GET ACTUAL VOLUME HERE
F0C2| 85 89          STA    IBSMOD      ; TELL OPSYS WHAT VOLUME WAS THERE
F0C4| A5 98          CORRECTVOL LDA    SECT       ; CHECK IF THIS IS THE RIGHT SECTOR
F0C6| C5 84          CMP    IBSECT
F0C8| D0C0          BNE    TRYADR2
F0CA| A5 87          LDA    IBCMD      ; READ OR WRITE?
F0CC| 4A             LSR    A          ; THE CARRY WILL TELL
F0CD| 902A          BCC    WRIT      ; CARRY WAS SET FOR READ OPERATION,
F0CF| 20 48F1        JSR    READ16     ; CLEARED FOR WRITE
F0D2| B0B6          BCS    TRYADR2     ; CARRY SET UPON RETURN IF BAD READ
F0D4| AD DFFF
F0D7| 29 7F          LDA    ENVIRON
F0D9| 8D DFFF
F0DC| 20 0FF3        STA    ENVIRON      ; SET TWO MEGAHERTZ
F0DF| A6 81          JSR    POSTNIB16   ; DO PARTIAL POSTNIBBLE CONVERSION
F0E1| B0A7          LDX    IBslot      ; RESTORE SLOTNUM INTO X
F0E3| 18             BCS    TRYADR2     ; CHECKSUM ERROR
F0E4| A9 00          ALLDONE CLC
F0E6| 9003          LDA    #00      ; NO ERROR
F0E8| A9 82          DRVERR LDA    #IBDERR   ; SKIP OVER NEXT BYTE WITH BIT OPCODE
F0EA| 38             HNDLERR SEC
F0EB| 85 88          ALLDONE1 STA   IBSTAT      ; INDICATE AN ERROR
F0ED| BD 88C0        LDA    MOTOROFF,X  ; GIVE HIM ERROR
F0F0| 20 AAF1        JSR    CHKINT     ; TURN IT OFF
F0F3| A5 9F          LDA    ENVTEMP    ; BRANCH TO CHECK FOR INTERRUPTS
F0F5| 8D DFFF
F0F8| 60             STA    ENVIRON    ; RESTORE ORIGINAL ENVIRONMENT
F0F9|               RTS
F0FC| 20 16F2        WRIT   JSR    WRITE16    ; WRITE NYBBLES NOW
F0FE| 90E5          BCC    ALLDONE
F0FE| A9 81          LDA    #IBWPER   ; IF NO ERRORS
F100| 50E8          BVC    HNDLERR   ; DISK IS WRITE PROTECTED!!
F102| D086          BNE    TRYADR2   ; TAKEN IF TRULY WRITE PROTECT ERROR
F104|               ; OTHERWISE ASSUME AN INTERRUPT MESSED THINGS UP
F104|               ; THIS IS THE 'SEEK' ROUTINE
F104|               ; SEEKS TRACK 'N' IN SLOT #X/$10
F104|               ; IF DRIVENO IS NEGATIVE, ON DRIVE 0
F104|               ; IF DRIVENO IS POSITIVE, ON DRIVE 1
F104|               ;
F104| 0A             MYSEEK  ASL    A          ; ASSUME TWO PHASE STEPPER.
F105| 85 99          SEEK1   STA    TRKN1     ; SAVE DESTINATION TRACK(*2)
F107| 20 18F1        JSR    ALLOFF    ; TURN ALL PHASES OFF TO BE SURE.
F10A| 20 3EF1        JSR    DRVINDX   ; GET INDEX TO PREVIOUS TRACK FOR CURRENT DRIVE
F10D| B5 85          LDA    DRVOTRK,X
F10F| 85 8C          STA    CURTRK    ; THIS IS WHERE I AM
F111| A5 99          LDA    TRKN1     ; AND WHERE I'M GOING TO
F113| 95 85          STA    DRVOTRK,X
F115| 20 00F4        GOSEEK  JSR    SEEK      ; GO THERE!
F118| A0 03          ALLOFF  LDY    #03      ; TURN OFF ALL PHASES BEFORE RETURNING
F11A| 98             NXOFF   TYA
F11B| 20 4AF4        JSR    CLRPHASE  ; (SEND PHASE IN ACC.)
F11E| 88             DEY
F11F| 10F9          BPL    NXOFF     ; CARRY IS CLEAR, PHASES SHOULD BE TURNED OFF
F121| 46 8C          LSR    CURTRK    ; DIVIDE BACK NOW
F123| 18             CLC
F124| 60             RTS
F125|               ; THIS SUBROUTINE SETS THE SLOT DEPENDENT TRACK
F125|               ; LOCATION
F125|               ;
F125| 20 3EF1        SETTRK  JSR    DRVINDX   ; GET INDEX TO DRIVE NUMBER
F128| 95 85          STA    DRVOTRK,X
F12A| 60             RTS
F12B|               ;
F12B|               ; ****
F12B|               ; SUBR TO TELL IF MOTOR IS STOPPED
F12B|               ;
F12B|               ; IF MOTOR IS STOPPED, CONTROLLER'S
F12B|               ; SHIFT REG WILL NOT BE CHANGING.
F12B|               ;
F12B|               ; RETURN Y=0 AND ZERO FLAG SET IF IT IS STOPPED.
F12B|               ;
F12B|               ; ****
F12B|               ;
F12B| A0 00          CHKDRV  LDY    #00      ; INIT LOOP COUNTER
F12D| BD 8CC0        CHKDRV1 LDA    Q6L,X    ; READ THE SHIFT REG

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"49.PICT" 681 KB 2001-09-10 dpi: 600h x 600v pix: 4663h x 6177v

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F130| 20 3DF1      JSR    CKDRTS   ; DELAY
F131| 48          PHA
F134| 68          PLA
F135| DD 8CC0      CMP    Q6L,X    ; HAS SHIFT REG CHANGED?
F138| D003        BNE    CKDRTS   ; YES, MOTOR IS MOVING
F13A| 88          DEY
F13B| D0F0        BNE    CHKDRV1 ; NO, DEC RETRY COUNTER
F13D| 60          RTS    CKDRTS   ; AND TRY 256 TIMES
F13E|             ; THEN RETURN
F13E| 48          ; PRESERVE ACC.
F13F| 8A          TXA
F140| 4A          LSR    A
F141| 4A          LSR    A
F142| 4A          LSR    A
F143| 05 82      ORA    IBDRVN  ; FOR DRIVE 0 OR 1
F145| AA          TAX
F146| 68          PLA
F147| 60          RTS    PLA      ; INTO X FOR INDEX TO TABLE
F148|             ; RESTORE ACC.

F148| ;*****
F148| ; NOTE: FORMATTING ROUTINES
F148| ; NOTE INCLUDED FOR SOS
F148| ;*****
F148| ;*****
F148| ; READ SUBROUTINE
F148| ; (16-SECTOR FORMAT)
F148| ;*****
F148| ; READS ENCODED BYTES
F148| ; INTO NBUF1 AND NBUF2
F148| ;*****
F148| ; FIRST READS NBUF2
F148| ; HIGH TO LOW,
F148| ; THEN READS NBUF1
F148| ; LOW TO HIGH.
F148| ;-----
F148| ; ON ENTRY
F148| ; X-REG: SLOTNUM
F148| ; TIMES $10.
F148| ; READ MODE (Q6L, Q7L)
F148| ;-----
F148| ; ON EXIT
F148| ; CARRY SET IF ERROR
F148| ; IF NO ERROR:
F148| ; A-REG HOLDS $AA.
F148| ; X-REG UNCHANGED.
F148| ; Y-REG HOLDS $00.
F148| ; CARRY CLEAR.
F148| ; CAUTION
F148| ; OBSERVE
F148| ; 'NO PAGE CROSS'
F148| ; WARNINGS ON
F148| ; SOME BRANCHES!!
F148| ; ASSUMES
F148| ; 1 USEC CYCLE TIME
F148| ;*****
F148| A0 20      READ16   LDY    #20      ; 'MUST FIND' COUNT.
F14A| 88          RSYNC    DEY
F14B| F06A        BEQ    RDERR   ; IF CAN'T FIND MARKS.
F14D| BD 8CC0      RD1    LDA    Q6L,X   ; THEN EXIT WITH CARRY SET
F150| 10FB        BPL    RD1    ; READ NIBL.
F152| 49 D5      RSYNC1   EOR    #0D5    ; *** NO PAGE CROSS! ***
F154| D0F4        BNE    RSYNC   ; DATA MARK1?
F156| EA          NOP
F157| BD 8CC0      RD2    LDA    Q6L,X   ; LOOP IF NOT.
F15A| 10FB        BPL    RD2    ; DELAY BETWEEN NIBLS.
F15C| C9 AA      CMP    #0AA    ; *** NO PAGE CROSS! ***
F15E| D0F2        BNE    RSYNC1  ; DATA MARK 2?
F160| A0 55      LDY    #055    ; (IF NOT, IS IT DM1?)
F162| EA          NOP    ( ADDED NIBL DELAY)
F163| BD 8CC0      RD3    LDA    Q6L,X   ; INIT NBUF2 INDEX.
F166| 10FB        BPL    RD3    ; DELAY BETWEEN NIBLS.
F168| C9 AD      CMP    #0AD    ; *** NO PAGE CROSS! ***
F16A| D0E6        BNE    RSYNC1  ; DATA MARK 3?
F16C|             ; (IF NOT, IS IT DM1?)


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← Seems like "Note"
should be "Not"

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F16C| EA NOP ; DELAY BETWEEN NIBLS.
F16D| EA NOP ; DELAY BETWEEN NIBLS.
F16E| BD 8CCØ RD4 LDA Q6L,X
F171| 1ØFB BPL RD4 ; *** NO PAGE CROSS! ***
F173| 99 Ø2Ø3 STA NBUF2,Y ; STORE BYTES DIRECTLY
F176| AD EFFF LDA INTERRUPT ; POLL INTERRUPT LINE
F179| Ø5 8B ORA IMASK ; (THIS MAY BE USED TO INVALIDATE POLL)
F17B| 1Ø37 BPL GOSERV
F17D| 88 DEY
F17E| 1ØEE BPL RD4 ; INDEX TO NEXT
F18Ø| C8 RD5 INY ; (FIRST TIME Y=Ø)
F181| BD 8CCØ RD5A LDA Q6L,X ; GET ENCODED BYTES OF NBUF1
F184| 1ØFB BPL RD5A
F186| 99 ØØØØ STA NBUF1,Y
F189| AD EFFF LDA INTERRUPT ; POLL INTERRUPT LINE
F18C| Ø5 8B ORA IMASK ; (THIS MAY BE USED TO INVALIDATE POLL)
F18E| 1Ø24 BPL GOSERV
F19Ø| CØ E4 CPY #ØE4 ; WITHIN 1 MS OF COMPLETION?
F192| DØEC BNE RD5
F194| C8 INY
F195| BD 8CCØ RD6 LDA Q6L,X ; NO POLL FROM NOW ON
F198| 1ØFB BPL RD6
F19A| 99 ØØØØ STA NBUF1,Y
F19D| C8 INY ; FINISH OUT NBUF1 PAGE
F19E| DØF5 BNE RD6
F1AØ| BD 8CCØ RDCKSUM LDA Q6L,X ; GET CHECKSUM BYTE.
F1A3| 1ØFB BPL RDCKSUM
F1A5| 85 96 STA CKSUM
F1A7| 2Ø Ø1F2 JSR RDA6 ; CHECK BIT SLIP MARKS
F1AA| ;
F1AA| ; CHECK FOR INTERRUPTS
F1AA| ;
F1AA| 24 8B CHKINT BIT IMASK ; SHOULD INTERRUPTS BE ALLOWED?
F1AC| 1ØØ4 BPL $Ø1Ø ; YES, ALLOW THEM.
F1AE| 24 8F BIT Ø8F
F1BØ| 1ØØ1 BPL $Ø2Ø
F1B2| 58 $Ø1Ø CLI
F1B3| 6Ø $Ø2Ø RTS
F1B4| ;
F1B4| 2Ø AAF2 GOSERV JSR SERVICE ; GO TO SERVICE INTERRUPT
F1B7| 38 RDERR SEC
F1B8| 6Ø RTS
F1B9| ;
F1B9| ;*****
F1B9| ; READ ADDRESS FIELD *
F1B9| ; SUBROUTINE *
F1B9| ; (16-SECTOR FORMAT) *
F1B9| ;
F1B9| ;*****
F1B9| ; READS VOLUME, TRACK *
F1B9| ; AND SECTOR *
F1B9| ;
F1B9| ; ---- ON ENTRY ---- *
F1B9| ;
F1B9| ; XREG: SLOTNUM TIMES $1Ø *
F1B9| ;
F1B9| ; READ MODE (Q6L, Q7L) *
F1B9| ;
F1B9| ; ---- ON EXIT ---- *
F1B9| ;
F1B9| ; CARRY SET IF ERROR *
F1B9| ;
F1B9| ; IF NO ERROR:
F1B9| ; A-REG HOLDS $AA.
F1B9| ; Y-REG HOLDS $ØØ.
F1B9| ; X-REG UNCHANGED.
F1B9| ; CARRY CLEAR.
F1B9| ;
F1B9| ; CSSTV HOLDS CHKSUM,
F1B9| ; SECTOR, TRACK, AND *
F1B9| ; VOLUME READ. *
F1B9| ;
F1B9| ; USES TEMPS COUNT,
F1B9| ; LAST, CSUM, AND *
F1B9| ; 4 BYTES AT CSSTV. *
F1B9| ;
F1B9| ; ---- EXPECTS ---- *
F1B9| ;
F1B9| ; ORIGINAL 1Ø-SECTOR *
F1B9| ; NORMAL DENSITY NIBLS *
F1B9| ; (4-BIT), ODD BITS,
F1B9| ; THEN EVEN *
F1B9| ;
F1B9| ; ---- CAUTION ---- *
F1B9| ;
F1B9| ; OBSERVE *
F1B9| ; 'NO PAGE CROSS' *
F1B9| ; WARNINGS ON *

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F216| ; ----- ASSUMES ----- *
F216| ; 1 USEC CYCLE TIME *
F216| ; ****
F216| ;
F216| 38      WRITE16  SEC      ; ANTICIPATE WPROT ERR.
F217| B8      CLV      ; TO INDICATE WRITE PROTECT ERROR INSTEAD OF
F218| ; INTERRUPT
F218| BD 8DCØ    LDA      Q6H,X
F218| BD 8ECØ    LDA      Q7L,X
F21E| 3ØF5      BMI      WEXIT
F22Ø| A9 FF      WRIT1   LDA      #ØFF
F222| 9D 8FCØ    STA      Q7H,X
F225| 1D 8CCØ    ORA      Q6L,X
F228| AØ Ø4      LDY      #Ø4
F22A| EA      NOP      ; (2) FOR FIVE NIBLS.
F22B| 48      PHA      ; (2)
F22C| 68      PLA      ; (3)
F22D| 48      WSYNC   PHA      ; (4) EXACT TIMING
F22E| 68      PLA      ; (3)
F22F| 2Ø BBF2    JSR      WNIBL7 ; (13,9,6) WRITE SYNC
F232| 88      DEY      ; (2)
F233| DØF8      BNE      WSYNC   ; (2*) MUST NOT CROSS PAGE!
F235| A9 D5      LDA      #ØD5
F237| 2Ø BAF2    JSR      WNIBL9 ; (15,9,6)
F23A| A9 AA      LDA      #ØAA
F23C| 2Ø BAF2    JSR      WNIBL9 ; (15,9,6)
F23F| A9 AD      LDA      #ØAD
F241| 2Ø BAF2    JSR      WNIBL9 ; (15,9,6)
F244| AØ 55      LDY      #55
F246| EA      NOP      ; (2)
F247| EA      NOP      ; (2)
F248| EA      NOP      ; (2)
F249| DØØ8      BNE      VRYFRST ; (3) BRANCH ALWAYS
F24B| AD EFFF    WINTRPT LDA      INTERRUPT ; (4) POLL INTERRUPT LINE
F24E| Ø5 8B      ORA      IMASK   ; (3)
F25Ø| 38      SEC      ; (2)
F251| 1Ø57      BPL      SERVICE ; (2) BRANCH IF INTERRUPT HAS OCCURED
F253| 3ØØØ    VRYFRST BMI      WRTFRST ; (3) FOR TIMING.
F255| B9 ØØØØ    WRTRST  LDA      NBUF2,Y
F258| 9D 8DCØ    STA      Q6H,X
F25B| BD 8CCØ    LDA      Q6L,X
F25E| 88      DEY      ; (2)
F25F| 1ØEA      BPL      WINTRPT ; (3) (2 IF BRANCH NOT TAKEN)
F261| 98      TYA      ; (2) INSURE NO INTERRUPT THIS BYTE
F262| 3ØØØ    BMI      WMIDLE ; (3) BRANCH ALWAYS.
F264| AD EFFF    WNTRPT1 LDA      INTERRUPT ; (4) POLL INTERRUPT LINE
F267| Ø5 8B      WMIDLE  ORA      IMASK   ; (3)
F269| 38      SEC      ; (2)
F26A| 3ØØØ    BMI      WDATA2 ; (3) BRANCH IF NO INTERRUPT
F26C| 1ØØC      BPL      SERVICE ; GO SERVICE INTERRUPT.
F26E| C8      WDATA2  INY      ; (2)
F26F| B9 ØØØØ    LDA      NBUF1,Y
F272| 9D 8DCØ    STA      Q6H,X
F275| BD 8CCØ    LDA      Q6L,X
F278| CØ E4      CPY      #ØE4
F27A| DØE8      BNE      WNTRPT1 ; (2) WITHIN 1 MS OF COMPLETION?
F27C| EA      NOP      ; (3) (2) NO KEEP WRITTING AND POLLING.
F27D| C8      INY      ; (2)
F27E| EA      NOP      ; (2)
F27F| EA      NOP      ; (2)
F28Ø| 48      PHA      ; (4)
F281| 68      PLA      ; (3)
F282| B9 ØØØØ    LDA      NBUF1,Y ; (4) WRITE LAST OF ENCODED BYTES
F285| 9D 8DCØ    STA      Q6H,X
F288| BD 8CCØ    LDA      Q6L,X
F28B| A5 96      LDA      CKSUM
F28D| C8      INY      ; (2)
F28E| DØEE      BNE      WDATA3 ; (3) (2)
F29Ø| FØØØ    BEQ      WRCKSUM ; (3) BRANCH ALWAYS
F292| 2Ø BBF2    WRCKSUM JSR      WNIBL7 ; (13,9,6) GO WRITE CHECK SUM!?
F295| 48      PHA      ; (3)
F296| 68      PLA      ; (4)
F297| B9 CØF3    WRBITSLMK LDA      BITSLIPMK,Y ; (4) LOAD BIT SLIP MARK
F29A| 2Ø BDF2    JSR      WNIBL   ; (6,9,6)
F29D| C8      INY      ; (2)
F29E| CØ Ø4      CPY      #Ø4
F2AØ| DØF5      BNE      WRBITSLMK ; (2) (3)
F2A2| 18      CLC      ; (2)
F2A3| BD 8ECØ    NOWRITE LDA      Q7L,X
F2A6| BD 8CCØ    LDA      Q6L,X
F2A9| 6Ø      RTS      ; TO READ MODE.
F2AA| ;      SERVICE BIT      SEV      ; SET VFLAG TO INDICATE INTERRUPT
F2AD| 2C 54F3    JSR      NOWRITE ; TAKE IT OUT OF WRITE MODE!
F2BØ| A5 8F      LDA      Ø8F
F2B2| 1ØØØ    BPL      ØØØØ
F2B4| 85 8B      STA      IMASK

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"53.PICT" 657 KB 2001-09-10 dpi: 600h x 600v pix: 4639h x 6166v

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F2B6| C6 8F      $010    DEC    08F
F2B8| 58          CLI
F2B9| 60          RTS
F2BA| ;
F2BA| ;*****
F2BA| ;*****7-BIT NIBL WRITE SUBRS*****
F2BA| ;*****A-REG OR'D PRIOR EXIT*****
F2BA| ;*****CARRY CLEARED*****
F2BA| ;*****
F2BA| ;*****
F2BA| 18          WNIBL9    CLC      ; (2) 9 CYCLES, THEN WRITE
F2BB| 48          WNIBL7    PHA      ; (3) 7 CYCLES, THEN WRITE
F2BC| 68          WNIBL    PLA      ; (4)
F2BD| 9D 8DC0    WNIBL    STA     Q6H,X  ; (5) NIBL WRITE SUB
F2C0| 1D 8CC0    ORA     Q6L,X  ; (4) CLOBBERS ACC. NOT CARRY
F2C3| 60          RTS
F2C4| ;
F2C4| ;*****
F2C4| ;*****PRENIBILIZE SUBR*****
F2C4| ;*****16-SECTOR FORMAT*****
F2C4| ;
F2C4| ;*****CONVERTS 256 BYTES OF*****
F2C4| ;*****USER DATA IN (BUF) INTO*****
F2C4| ;*****ENCODED BYTES TO BE*****
F2C4| ;*****WRITTEN DIRECTLY TO DISK*****
F2C4| ;*****ENCODED CHECK SUM IN*****
F2C4| ;*****ZERO PAGE 'CKSUM'*****
F2C4| ;
F2C4| ;-----ON ENTRY-----
F2C4| ;
F2C4| ;BUF IS 2-BYTE POINTER
F2C4| ;TO 256 BYTES OF USER
F2C4| ;DATA.
F2C4| ;
F2C4| ;A-REG CHECK SUM.
F2C4| ;X-REG UNCERTAIN
F2C4| ;Y-REG HOLDS 0.
F2C4| ;CARRY SET.
F2C4| ;
F2C4| ;*****
F2C4| ;
F2C4| A2 02      PRENIB16   LDX    #02    ; START NBUF2 INDEX.
F2C6| A0 00      PRENIB1    LDY    #00    ; START USER BUF INDEX.
F2C8| 88          PRENIB1    DEY
F2C9| B1 9B      PRENIB1    LDA    (BUF),Y ; NEXT USER BYTE
F2CB| 4A          PRENIB1    LSR    A      ; SHIFT TWO BITS OF
F2CC| 3E 0103    PRENIB1    ROL    NBUF2-1,X ; CURRENT USER BYTE
F2CF| 4A          PRENIB1    LSR    A      ; INTO CURRENT NBUF2
F2D0| 3E 0103    PRENIB1    ROL    NBUF2-1,X ; BYTE.
F2D3| 99 0102    PRENIB1    STA    NBUF1+1,Y ; (6 BITS LEFT).
F2D6| E8          PRENIB1    INX    #56    ; FROM 0 TO $55
F2D7| E0 56      PRENIB1    CPX
F2D9| 90ED      PRENIB1    BCC    PRENIB1 ; BR IF NO WRAPAROUND
F2DB| A2 00      PRENIB1    LDX    #00    ; RESET NBUF2 INDEX
F2DD| 98          PRENIB1    TYA
F2DE| D0E8      PRENIB1    BNE    PRENIB1 ; (DONE IF ZERO)
F2E0| A0 56      PRENIB1    LDY    #56    ; (ACC=0 FOR CHECK SUM)
F2E2| 59 0003    PRENIB3    EOR    NBUF2-2,Y ; COMBINE WITH PREVIOUS
F2E5| 29 3F      PRENIB2    AND    #03F   ; STRIP GARBAGE BITS
F2E7| AA          PRENIB2    TAX
F2E8| BD 55F3    PRENIB2    LDA    NIBL,X  ; TO FORM RUNNING CHECK SUM
F2EB| 99 0103    PRENIB2    STA    NBUF2-1,Y ; GET ENCODED EQUIV.
F2EE| B9 0003    PRENIB2    LDA    NBUF2-2,Y ; REPLACE PREVIOUS
F2F1| 88          PRENIB2    DEY
F2F2| D0EE      PRENIB3    BNE    PRENIB3 ; RESTORE ACTUAL PREVIOUS
F2F4| 29 3F      PRENIB3    AND    #3F   ; LOOP UNTIL ALL OF NBUF2 IS CONVERTED.
F2F6| 59 0102    PRENIB4    EOR    NBUF1+1,Y ; NOW DO THE SAME FOR
F2F9| AA          PRENIB4    TAX
F2FA| BD 55F3    PRENIB4    LDA    NIBL,X  ; NIBBLE BUFFER 1
F2FD| 99 0002    PRENIB4    STA    NBUF1,Y ; TO DO ANY BACK TRACKING (NBUF1-1)
F300| B9 0102    PRENIB4    LDA    NBUF1+1,Y ; RECOVER THAT WHICH IS NOW 'PREVIOUS'
F303| C8          PRENIB4    INY
F304| D0F0      PRENIB4    BNE    PRENIB4 ; USE LAST AS CHECK SUM
F306| AA          PRENIB4    TAX
F307| BD 55F3    PRENIB4    LDA    NIBL,X
F30A| 85 96      PRENIB4    STA    CKSUM
F30C| 4C 4CF3    PRENIB4    JMP    SET1MEG ; ALL DONE.
F30F| ;
F30F| ;*****
F30F| ;*****POSTNIBLIZE SUBR*****
F30F| ;*****16-SECTOR FORMAT*****
F30F| ;
F30F| ;*****

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"54.PICT" 578 KB 2001-09-10 dpi: 600h x 600v pix: 4627h x 6178v

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F30F| ; POSTNIB16 SEC
F310| 38 LDY #55 ; FIRST CONVERT TO 6 BIT NIBBLES
F312| A0 55 LDA #00 ; INIT CHECK SUM
F314| A9 00 PNIBL1 LDX NBUF2,Y ; GET ENCODED BYTE
F317| BE 0203 EOR DNIBL,X
F31A| 5D 00F3 BMI SET1MEG ; SET 1 MHZ
F31C| 3030 STA NBUF2,Y ; REPLACE WITH 6 BIT EQUIV.
F31F| 88 DEY
F320| 10A6 BPL PRENIB1 ; LOOP UNTIL DONE WITH NIBBLE BUFFER 2
F322| C8 INY
F323| BE 0002 LDX NBUF1,Y ; NOW Y=0
F326| 5D 00F3 PNIBL2 EOR DNIBL,X
F329| 99 0002 STA NBUF1,Y ; NIBBLE BUFFER 1
F32C| C8 INY ; DO ALL 256 BYTES
F32D| D0F4 BNE PNIBL2
F32F| A6 96 LDX CKSUM ; MAKE SURE CHECK SUM MATCHES
F331| 5D 00F3 EOR DNIBL,X ; BETTER BE ZERO
F334| D016 BNE POSTERR ; BRANCH IF IT IS
F336| A2 56 POST1 LDX #56 ; INIT NBUF2 INDEX
F338| CA DEX ; NBUF IDX $55 TO $00
F339| 30FB BMI POST1 ; WRAPAROUND IF NEG .
F33B| B9 0002 LDA NBUF1,Y
F33E| 5E 0203 LSR NBUF2,X ; SHIFT 2 BITS FROM
F341| 2A ROL A ; CURRENT NBUF2 NIBL
F342| 5E 0203 LSR NBUF2,X ; CURRENT NBUF1
F345| 2A ROL A ; NIBL.
F346| 91 9B STA (BUF),Y ; BYTE OF USER DATA
F348| C8 INY ; NEXT USER BYTE
F349| D0ED BNE POST2
F34B| 18 CLC ; GOOD DATA
F34C| F34C POSTERR .EQU *
F34C| AD DFFF SET1MEG LDA ENVIRON
F34F| 09 80 ORA #ONEMEG ; SET TO ONE MEGAHERTZ CLOCK RATE
F351| 8D DFFF STA ENVIRON
F354| 60 SEV RTS ; (SEV USED TO SET VFLAG)

F355| ;*****
F355| ; 6-BIT TO 7-BIT *
F355| ; NIBL CONVERSION TABLE *
F355| ;*****
F355| ;*****
F355| ; CODES WITH MORE THAN *
F355| ; ONE PAIR OF ADJACENT *
F355| ; ZEROES OR WITH NO *
F355| ; ADJACENT ONES (EXCEPT *
F355| ; BIT 7) ARE EXCLUDED. *
F355| ;*****
F355| ;*****
F355| NIBL .BYTE 96,97,9A,9B,9D,9E,9F,0A6,0A7,0AB,0AC,0AD,0AE,0AF,0B2,0B3,0B4,0B5
F363| A6 A7 AB AC AD AE AF .BYTE 0B6,0B7,0B9,0BA,0BB,0BC,0BD,0BE,0BF,0CB,0CD,0CE,0CF,0D3,0D6,0D7
F363| B2 B3 B4 B5 .BYTE 0E9,0EA,0EB,0EC,0ED,0EE
F367| B6 B7 B9 BA BB BC BD .BYTE 0D9,0DA,0DB,0DC,0DD,0DE,0DF,0E5,0E6,0E7,0E9,0EA,0EB,0EC,0ED,0EE
F36E| BE BF CB CD CE CF D3 .BYTE 0F2,0F3,0F4,0F5,0F6,0F7,0F9,0FA,0FB,0FC,0FD,0FE,0FF
F375| D6 D7
F377| D9 DA DB DC DD DE DF
F37E| E5 E6 E7 E9 EA EB EC
F385| ED EE
F387| EF F2 F3 F4 F5 F6 F7
F38E| F9 FA FB FC FD FE FF
F395| ;*****
F395| ; 7-BIT TO 6-BIT *
F395| ; 'DENIBLIZE' TABL *
F395| ; (16-SECTOR FORMAT) *
F395| ;*****
F395| ; VALID CODES *
F395| ; $96 TO $FF ONLY. *
F395| ;*****
F395| ; CODES WITH MORE THAN *
F395| ; ONE PAIR OF ADJACENT *
F395| ; ZEROES OR WITH NO *
F395| ; ADJACENT ONES (EXCEPT *
F395| ; BIT 7) ARE EXCLUDED *
F395| ;*****
F395| ;*****
F395| F300 DNIBL .EQU REGRWTS+300
F395| 01 00 01 .BYTE 01,00,01
F398| 98 99 02 03 9C 04 05 .BYTE 98,99,02,03,9C,04,05,06,0A0,0A1,0A2,0A3,0A4,0A5,07,08,0A8
F39F| 06 A0 A1 A2 A3 A4 A5
F3A6| 07 08 A8
F3A9| A9 AA 09 0A 0B 0C 0D .BYTE 0A9,0AA,09,0A,0B,0C,0D,0B0,0B1,0E,0F,10,11,12,13,0B8,14,15
F3B0| B0 B1 0E 0F 10 11 12
F3B7| 13 B8 14 15
F3BB| 16 17 18 19 1A .BYTE 16,17,18,19,1A

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"55.PICT" 632 KB 2001-09-10 dpi: 600h x 600v pix: 4616h x 6213v

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F3C0| DE AA EB FF C4 C5 C6  BITSLIPMK .BYTE  0DE,0AA,0EB,0FF,0C4,0C5,0C6,0C7,0C8,0C9,0CA,1B,0CC,1C,1D,1E
F3C1| C7 C8 C9 CA 1B CC 1C
F3CE| 1D 1E
F3D0| D0 D1 D2 1F D4 D5 20 .BYTE  0D0,0D1,0D2,1F,0D4,0D5,20,21,0D8,22,23,24,25,26,27,28,0E0,0E1
F3D7| 21 D8 22 23 24 25 26
F3DE| 27 28 E0 E1
F3E2| E2 E3 E4 29 2A 2B E8 .BYTE  0E2,0E3,0E4,29,2A,2B,0E8,2C,2D,2E,2F,30,31,32,0F0,0F1,33,34
F3E9| 2C 2D 2E 2F 30 31 32
F3F0| F0 F1 33 34
F3F4| 35 36 37 38 F8 39 3A .BYTE  35,36,37,38,0F8,39,3A,3B,3C,3D,3E,3F
F3FB| 3B 3C 3D 3E 3F

F400|
F400| ;*****
F400| ; FAST SEEK SUBROUTINE
F400| ;*****
F400| ;*****
F400| ;----- ON ENTRY -----
F400| ; X-REG HOLDS SLOTNUM
F400| ; TIMES $10
F400| ; A-REG HOLDS DESIRED
F400| ; HALFTRACK.
F400| ; CURTRK HOLDS DESIRED
F400| ; HALFTRACK.
F400| ;----- ON EXIT -----
F400| ; A-REG UNCERTAIN.
F400| ; Y-REG UNCERTAIN.
F400| ; X-REG UNDISTURBED.
F400| ; CURTRK AND TRKN HOLD
F400| ; FINAL HALFTRACK.
F400| ; PRIOR HOLDS PRIOR
F400| ; HALFTRACK IF SEEK
F400| ; WAS REQUIRED.
F400| ; MONTIMEL AND MONTIMEH
F400| ; ARE INCREMENTED BY
F400| ; THE NUMBER OF
F400| ; 100 USEC QUANTUMS
F400| ; REQUIRED BY SEEK
F400| ; FOR MOTOR ON TIME
F400| ; OVERLAP.
F400| ;--- VARIABLES USED ---
F400| ; CURTRK, TRKN, COUNT,
F400| ; PRIOR, SLOTEMP
F400| ; MONTIMEL, MONTIMEH
F400| ;*****
F400| ;*****
F400| 85 9E
F402| C5 8C
F404| F042
F406| A9 00
F408| 85 95
F40A| A5 8C
F40C| 85 9D
F40E| 38
F40F| E5 9E
F411| F031
F413| B006
F415| 49 FF
F417| E6 8C
F419| 9004
F41B| 69 FE
F41D| C6 8C
F41F| C5 95
F421| 9002
F423| A5 95
F425| C9 09
F427| B002
F429| A8
F42A| 38
F42B| 20 48F4
F42E| B9 67F4
F431| 20 56F4
F434| A5 9D
F436| 18
F437| 20 4A4F
F43A| B9 70F4
F43D| 20 56F4
F440| E6 95

SEEK     STA     TRKN      ; SAVE TARGET TRACK
SEEK     CMP     CURTRK   ; ON DESIRED TRACK?
SEEK     BEQ     SETPHASE ; YES, ENERGIZE PHASE AND RETURN
SEEK2    LDA     #00
SEEK2    STA     TRKCNT   ; HALFTRACK COUNT.
SEEK2    LDA     CURTRK   ; SAVE CURTRK FOR
SEEK2    STA     PRIOR    ; DELAYED TURN OFF.
SEEK     SEC
SEEK     SBC     TRKN      ; DELTA-TRACKS.
SEEK     BEQ     SEEKEND  ; BR IF CURTRK=DESTINATION
SEEK     BCS     OUT       ; (MOVE OUT, NOT IN)
SEEK     EOR     #0FF     ; CALC TRKS TO GO.
SEEK     INC     CURTRK   ; DECR CURRENT TRACK (OUT)
SEEK     BCC     MINTST   ; (ALWAYS TAKEN).
SEEK     OUT    ADC     #0FE    ; CALC TRACKS TO GO.
SEEK     DEC     CURTRK   ; DECR CURRENT TRACK (OUT)
MINTST   CMP     TRKCNT   ; AND 'TRKS MOVED'
MINTST   BCC     MAXTST   ; MAXTST
MINTST   LDA     TRKCNT   ; AND 'TRKS MOVED'
MAXTST  CMP     #09
MAXTST  BCS     STEP2    ; IF TRKCNT>$08 LEAVE Y ALONE (Y=$08)
MAXTST  STEP   TAY
MAXTST  SEC
STEP2   JSR     SETPHASE
STEP2   LDA     ONTABLE, Y ; FOR 'ONTIME'
STEP2   JSR     MSWAIT   ; (100 USEC INTERVALS)
STEP2   LDA     PRIOR    ; FOR PHASE OFF
STEP2   JSR     CLRPHASE ; TURN OFF PRIOR PHASE
STEP2   LDA     OFFTABLE, Y ; THEN WAIT 'OFFTIME'
STEP2   JSR     MSWAIT   ; (100 USEC INTERVALS)
STEP2   INC     TRKCNT   ; 'TRACKS MOVED' COUNT.

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"56.PICT" 598 KB 2001-09-10 dpi: 600h x 600v pix: 4627h x 6189v

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F442| D0C6
F444| 20 56F4
F447| 18
F448| A5 8C
F44A| 29 03
F44C| 2A
F44D| 05 81
F44F| AA
F450| BD 80C0
F453| A6 81
F455| 60
F456| ; ****
F456| ; *****MSWAIT***** *
F456| ; MSWAIT SUBROUTINE *
F456| ; ****
F456| ; *****DELAYS A SPECIFIED ***** *
F456| ; NUMBER OF 100 USEC *
F456| ; INTERVALS FOR MOTOR *
F456| ; ON TIMING *
F456| ; ****
F456| ; ---- ON EXIT ---- *
F456| ; ****
F456| ; A-REG HOLDS $00 *
F456| ; X-REG HOLDS $00 *
F456| ; Y-REG UNCHANGED *
F456| ; CARRY SET *
F456| ; ****
F456| ; MONTIMEL, MONTIMEH *
F456| ; ARE INCREMENTED ONCE *
F456| ; PER 100 USEC INTERVAL *
F456| ; FOR MOTOR ON TIMING *
F456| ; ****
F456| ; ---- ASSUMES ---- *
F456| ; ****
F456| ; 1 USEC CYCLE TIME *
F456| ; ****
F456| ; ****
F456| A2 11
F458| CA
F459| D0FD
F45B| E6 99
F45D| D002
F45F| E6 9A
F461| 38
F462| E9 01
F464| D0F0
F466| 60
F467| ; ****
F467| ; *****PHASE ON-, OFF-TIME***** *
F467| ; TABLES IN 100-USEC *
F467| ; INTERVALS. (SEEK) *
F467| ; ****
F467| ; ****
F467| 01 30 28 24 20 1E 1D ONTABLE .BYTE 01,30,28,24,20,1E,1D,1C,1C
F46E| 1C 1C
F470| 70 2C 26 22 1F 1E 1D OFFTABLE .BYTE 70,2C,26,22,1F,1E,1D,1C,1C
F477| 1C 1C
F479| 86 83
F47B| A0 05
F47D| 48
F47E| 0A
F47F| 26 83
F481| 88
F482| D0FA
F484| 68
F485| 29 07
F487| A8
F488| B9 A0F4
F48B| 85 84
F48D| 20 00F0
F490| B00B
F492| E6 86
F494| E6 84
F496| E6 84
F498| 20 00F0
F49B| C6 86
F49D| A5 88
F49F| 60
F4A0| ; ****
F4A0| 00 04 08 0C 01 05 09 SECTABL .BYTE 00,04,08,0C,01,05,09,0D
F4A7| 0D
F4A8| ; ****

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F4A8| ; JOYSTICK READ ROUTINE *
F4A8| ; ****
F4A8| ; ENTRY ACC= COUNT DOWN HIGH *
F4A8| ; X&Y= DON'T CARE *
F4A8| ; EXIT ACC= TIMER HIGH BYTE *
F4A8| ; Y= TIMER LOW BYTE *
F4A8| ; CARRY CLEAR *
F4A8| ; IF CARRY SET, ROUTINE *
F4A8| ; WAS INTERRUPTED & *
F4A8| ; ACC & Y ARE INVALID *
F4A8| ; ****
F4A8| ;
F4A8| FFD9 TIMLATCH .EQU 0FFD9
F4A8| FFD8 TIMER1L .EQU 0FFD8
F4A8| FFD9 TIMER1H .EQU 0FFD9
F4A8| C066 JOYRDY .EQU 0C066
F4A8| ;
F4A8| F4A8 ANALOG .EQU * ; CARRY SHOULD BE SET!
F4A8| 8D D9FF STA TIMLATCH ; START THE TIMER!
F4AB| AD EFFF ANLOG1 LDA INTERRUPT
F4AE| 2D 66C0 AND JOYRDY ; WAIT FOR ONE OR THE OTHER TO GO LOW
F4B1| 30F8 BMI ANLOG1
F4B3| AD 66C0 LDA JOYRDY ; WAS IT REALLY THE JOYSTICK?
F4B6| 300C BMI GOODTIME ; NOPE, WHAT TIME IS IT?
F4B8| 18 CLC ; TIME'S A SLIP SLIDIN AWAY
F4B9| AD D9FF LDA TIMER1H ; NOW, WHAT TIME IS IT?
F4BC| AC D8FF LDY TIMER1L
F4BF| 1003 BPL GOODTIME ; TIME WAS VALID!
F4C1| AD D9FF LDA TIMER1H ; HI BYTE CHANGED
F4C4| 60 GOODTIME RTS
F4C5| .END

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SYMBOL TABLE DUMP

AB - Absolute	LB - Label	UD - Undefined	MC - Macro
RF - Ref	DF - Def	PR - Proc	FC - Func
PB - Public	PV - Private	CS - Consts	

ALDONE1	LB F0EB	ALLDONE	LB F0E3	ALLOFF	LB F118	ANALOG	LB F4A8	ANLOG1	LB F4AB
BITSLIPM	LB F3C0	BLOCKIO	LB F479	BUF	AB 009B	CHKDRV	LB F12B	CHKDRV1	LB F12D
CHKINT	LB F1AA	CKDRTS	LB F13D	CKSUM	AB 0096	CLRPHASE	LB F44A	CONWAIT	LB F054
CORRECTV	LB F0C4	COUNT	AB 0095	CSSTV	AB 0097	CSUM	AB 0099	CSUM1	AB 0097
CURTRK	AB 008C	DISKIO	PR ----	DNIBL	LB F300	DRIVSEL	LB F035	DRV1EN	AB C08B
DRVERR	LB F0E8	DRVINDEX	LB F13E	DRVOPEN	AB C08A	DRVOTRK	AB 0085	DRVWAIT	LB F041
DVMOT	AB 0000	ENVIRON	AB FFFD	ENVTEMP	AB 009F	GOCAL	LB F0A7	GOCAL1	LB F0A6
GOODTIME	LB F4C4	GOSEEK	LB F115	GOSEVR	LB F1B4	HNDLERR	LB F0EA	HRDERRS	AB 0080
IBBUFP	AB 0085	IBCMD	AB 0087	IBDERR	AB 0082	IBDRVN	AB 0082	IBNDRV	AB 0080
IBRERR	AB 0083	IBSECT	AB 0084	IBSLOT	AB 0081	IBSMOD	AB 0089	IBSTAT	AB 0088
IBTRK	AB 0083	IBWPER	AB 0081	IMASK	AB 000B	INTERUPT	AB FFEF	IOBPDN	AB 008A
JOYRDY	AB C066	LAST	AB 0095	MAXTST	LB F425	MIN1ST	LB F41F	MONTIMEH	AB 009A
MONTIMEL	AB 0099	MOTOF	LB F052	MOTOROFF	AB C088	MOTORON	AB C089	MSW1	LB F458
MSW2	LB F461	MSWAIT	LB F456	MYSEEK	LB F104	NBUF1	AB 0000	NBUF2	AB 0302
NIBL	LB F355	NODRIVER	LB F064	NOWRITE	LB F2A3	NXOFF	LB F11A	OFFTABLE	LB F470
OK	LB F048	ONEMEG	AB 0080	ONTABLE	LB F467	OUT	LB F41B	PHASEOFF	AB C080
PHASEON	AB C081	PHASON	AB C081	PHSOFF	AB C080	PNIBL1	LB F314	PNIBL2	LB F323
POST1	LB F336	POST2	LB F338	POSTERR	LB F34C	POSTNIB1	LB F30F	PRENIB1	LB F2C8
PRENIB16	LB F2C4	PRENIB2	LB F2B5	PRENIB3	LB F2E2	PRENIB4	LB F2F6	PRIOR	AB 009D
Q6H	AB C08D	Q6L	AB C08C	Q7H	AB C08F	Q7L	AB C08E	QUIT	LB F49D
RD1	LB F14D	RD2	LB F157	RD3	LB F163	RD4	LB F16E	RD5	LB F180
RD5A	LB F181	RD6	LB F195	RDA1	LB F1C4	RDA2	LB F1CE	RDA3	LB F1D9
RDA4	LB F1E7	RDA5	LB F1EF	RDA6	LB F201	RDA7	LB F20B	RDADR16	LB F1B9
RDAFLD	LB F1E5	RDASN1	LB F1C9	RDASYN	LB F1BD	RDCKSUM	LB F1A0	RDERR	LB F1B7
RDEXIT	LB F214	RDRIGHT	LB F0AC	READ16	LB F148	REGRWTS	LB F000	RETRYCNT	AB 0093
RSYNC	LB F14A	RSYNC1	LB F152	RTTRK	LB F0C0	SECT	AB 0098	SECTABL	LB F4A0
SEEK	LB F400	SEEK1	LB F105	SEEK2	LB F40A	SEEKCNT	AB 0094	SEEKEND	LB F444
SEEKRTS	LB F455	SERVICE	LB F2AA	SETIMEG	LB F34C	SETPHASE	LB F448	SETTRK	LB F125
SEV	LB F354	STEP	LB F429	STEP2	LB F42B	TEMP	AB 0097	TIMER1H	AB FFD9
TIMER1L	AB FFD8	TIMLATCH	AB FFD9	TRACK	AB 0099	TRKCNT	AB 0095	TRKN	AB 009E
TRKN1	AB 0099	TRKSEC	LB F47E	TRYADR	LB F083	TRYADR2	LB F08A	TRYTRK	LB F069
TRYTRK2	LB F07F	TWOMEQ	AB 007F	VOLUME	AB 009A	VRYFRST	LB F253	WDATA2	LB F26E
WDATA3	LB F27E	WEXIT	LB F215	WINTRPT	LB F24B	WMIDDLE	LB F267	WNIBL	LB F2BD
WNIBL7	LB F2BB	WNIBL9	LB F2BA	WNTRPT1	LB F264	WRBITSLM	LB F297	WRCKSUM	LB F292
WRIT	LB F0E9	WRIT1	LB F220	WRITE16	LB F216	WRTFRST	LB F255	WSYNC	LB F22D

Assembly complete: 1076 lines
 0 Errors flagged on this Assembly

6502 OPCODE STATIC FREQUENCIES

ADC : 1 m
 AND : 8 | *****

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ASL :	3		***
BCC :	1Ø		*****
BCS :	7		*****
BEQ :	8		*****
BIT :	3		**
BMI :	1Ø		*****
BNE :	38		*****
BPL :	28		*****
BVC :	1	m	
CLC :	9		*****
CLI :	2		*
CLV :	1	m	
CMP :	14		*****
CPX :	1	m	
CPY :	4		***
DEC :	5		***
DEX :	2		*
DEY :	13		*****
EOR :	8		*****
INC :	1Ø		*****
INX :	2		*
INY :	12		*****
JMP :	2		*
JSR :	39		*****
LDA :	86	M	*****
LDX :	12		*****
LDY :	18		*****
LSR :	9		*****
NOP :	13		*****
ORA :	9		*****
PHA :	1Ø		*****
PHP :	4		***
PLA :	11		*****
PLP :	3		**
ROL :	7		*****
ROR :	6		*****
RTS :	16		*****
SBC :	2		*
SEC :	9		*****
SEI :	1	m	
STA :	42		*****
STX :	1	m	
STY :	3		**
TAX :	5		***
TAY :	3		**
TXA :	1	m	
TYA :	4		***

Minimum frequency = 1
 Maximum frequency = 86

Average frequency = 1Ø

Unused opcodes:

BRK BVS CLD RTI SED TSX TXS

Program opcode usage: 87 %

(1.ØØ) That's all, Folks ...

Source Code Listing
for
Apple //

**ROM
Diagnostics**

David T. Craig
736 Edgewater
Wichita, Kansas 67230

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W=Walt

Broedner
later designed
the hardware
for the
Apple IIe
computer
which was
released in
January 1983

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F590 20 38F7		JSR	STRWT	; THIS SUBROUTINE
F593 A2 00	ERRLP1	LDX	#00	; PRINT 'RAM'
F595 86 5D		STX	CV	; SET CURSOR TO 2ND LINE
F597 A9 04		LDA	#04	; SPACE CURSOR OUT 3
F599 20 C7FB		JSR	SETCVH	; (X STILL=0 ON RETURN)
F59C 20 38F7		STRWT		; THE SAME SUBROUTINE
F59F A2 07		LDX	#07	; FOR BYTES 7 - 0 IN
F5A1 F5A1	RAMWT1	.EQU	*	
F5A1 B5 10		LDA	ZRPG1,X	; OUT EACH BIT AS A
F5A3 A0 08		LDY	#08	; '0' OR '1' FOR INDICATE BAD OR MISSING RAM
F5A5 0A	RAMWT2	ASL	A	; CHIPS SUBROUTINE 'RAM' RAM
F5A6 48		PHA		; SETS UP THESE BYTES
F5A7 A9 AE		LDA	#0AE	; LOAD A '.' TO ACC.
F5A9 9002		BCC	RAMWT4	
F5AB A9 31		LDA	#31	; LOAD A '1' TO ACC.
F5AD 20 25FC	RAMWT4	JSR	COUT	; AND PRINT IT
F5B0 68		PLA		; RESTORE BYTE
F5B1 88		DEY		; AND ROTATE ALL 8
F5B2 D0F1		BNE	RAMWT2	; TIMES
F5B4 20 07FD		JSR	CRROUT1	; CLEAR TO END OF LINE.
F5B7 CA		DEX		
F5B8 10E7		BPL	RAMWT1	
F5BA ;				
F5BA ; ZPG & STK TEST				
F5BA ;				
F5BA 9A		TXS		
F5BB 8C EFFF		STY	BNKSW	
F5BE 98	ZP1	TYA		
F5BF 8D D0FF		STA	ZPREG	
F5C2 85 FF		STA	STK0	
F5C4 C8		INY		
F5C5 98		TYA		
F5C6 48		PHA		
F5C7 68		PLA		
F5C8 C8		INY		
F5C9 C0 20		CPY	#20	
F5CB D0F1		BNE	ZP1	
F5CD A0 00		LDY	#00	
F5CF 8C D0FF		STY	ZPREG	
F5D2 86 18		STX	PTRLO	
F5D4 E8	ZP2	INX		
F5D5 86 19		STX	PTRHI	
F5D7 8A		TXA		
F5D8 D1 18		CMP	(PTRLO), Y	
F5DA D006		BNE	ZP3	
F5DC E0 1F		CPX	#1F	
F5DE D0F4		BNE	ZP2	
F5E0 F005		BEQ	ROMTST	
F5E2 F5E2	ZP3	.EQU	*	; CHIP IS THERE, BAD ZERO AND STACK
F5E2 A2 1A		LDX	#1A	; SO PRINT 'ZP' MESSAGE
F5E4 20 7BF7		JSR	MESSERR	; & SET FLAG (2MHZ MODE)
F5E7 ;				
F5E7 ; ROM TEST ROUTINE				
F5E7 ;				
F5E7 A9 00		ROMTST	LDA	#00 ; SET POINTERS TO
F5E9 A8			TAY	
F5EA A2 F0			LDX	#0F0 ; \$F000
F5EC 85 18			STA	PTRLO
F5EE 86 19			STX	PTRHI ; SET X TO \$FF
F5F0 A2 FF			LDX	#0FF ; FOR WINDOWING I/O
F5F2 51 18	ROMTST1	EOR	(PTRLO), Y ; COMPUTE CHKSUM ON	
F5F4 E4 19		CPX	PTRHI ; EACH ROM BYTE,	
F5F6 D006		BNE	ROMTST2 ; WINDOW OUT	
F5F8 C0 BF		CPY	#0BF ; RANGES FFC0-FFEF	
F5FA D002		BNE	ROMTST2	
F5FC A0 EF		LDY	#0EF	
F5FE C8	ROMTST2	INY		
F5FF D0F1		BNE	ROMTST1	
F601 E6 19		INC	PTRHI	
F603 D0ED		BNE	ROMTST1	
F605 A8		TAY		; TEST ACC. FOR 0
F606 F005		BEQ	VIATST ; YES, NEXT TEST	
F608 A2 03		LDX	#03 ; PRINT 'ROM' AND	
F60A 20 7BF7		JSR	MESSERR ; SET ERROR	
F60D ;				
F60D ; VIA TEST ROUTINE				
F60D ;				
F60D 18	VIATST	CLC		; SET UP FOR ADDING BYTES
F60E D8		CLD		
F60F AD E0FF		LDA	SYSE0 ; MASK OFF INPUT BITS	
F612 29 3F		AND	#3F ; AND STORE BYTE IN	
F614 85 18		STA	PTRLO ; TEMPOR. LOCATION	
F616 AD EFFF		LDA	BNKSW ; MASK OFF INPUT BITS	
F619 29 4F		AND	#4F ; AND ADD TO STORED	
F61B 65 18		ADC	PTRLO ; BYTE IN TEMP. LOC.	
F61D 6D D0FF		ADC	ZPREG ; ADD REMAINING	
F620 85 18		STA	PTRLO ; REGISTERS OF THE	
F622 AD DFFF		LDA	SYSD1 ; VIA'S	
F625 29 5F		AND	#5F ; (MASK THIS ONE)	
F627 65 18		ADC	PTRLO ; AND TEST	

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F629| 6D D2FF      ADC  SYSD2    ; TO SEE
F62C| 6D D3FF      ADC  SYSD3    ; IF THEY AGREE
F62F| 6D E2FF      ADC  SYSE2    ; WITH THE RESET
F632| 6D E3FF      ADC  SYSE3    ; CONDITION.
F635| C9 E1       CMP  #0E0+ROM ; =E1?
F637| F005        BEQ  ACIA     ; YES, NEXT TEST
F639| A2 Ø6       LDX  #Ø6     ; NO, PRINT 'VIA' MESS
F63B| 2Ø 7BF7      JSR  MESSERR ; AND SET ERROR FLAG
F63E|
F63E| ; ACIA TEST
F63E| ;
F63E| 18          ACIA   CLC   ; SET UP FOR ADDITION
F63F| A9 9F       LDA   #9F     ; MASK INPUT BITS
F641| 2D F1CØ      AND   ACIAST   ; FROM STATUS REG
F644| 6D F2CØ      ADC   ACIACM   ; AND ADD DEFAULT STATES
F647| 6D F3CØ      ADC   ACIACN   ; OIF CONTROL AND COMMAND
F64A| C9 1Ø       CMP  #1Ø     ; REGS. =1Ø?
F64C| F005        BEQ  ATD     ; YES, NEXT TEST
F64E| A2 Ø9       LDX  #Ø9     ; NO, 'ACIA' MESSAGE AND
F650| 2Ø 7BF7      JSR  MESSERR ; THEN SET ERROR FLAG
F653|
F653| ; A/D TEST ROUTINE
F653| ;
F653| A9 CØ       ATD   LDA   #ØCØ
F655| 8D DCFF      STA   ØFFDC
F658| AD 5ACØ      LDA   PDLEN+2
F65B| AD 5ECØ      LDA   PDLEN+6
F65E| AD 5CCØ      LDA   PDLEN+4
F661| AØ 2Ø       LDY   #2Ø
F663| 88          ADCTST1 DEY   ; WAIT FOR 4Ø USEC
F664| DØFD        BNE  ADCTST1
F666| AD 5DCØ      LDA   PDLEN+5 ; SET A/D RAMP
F669| C8          F66A| FØØA        INY   ; COUNT FOR CONVERSION
F66C| AD 66CØ      BEQ  ADCERR
F66F| 3ØF8        LDA   ATDO    ; IF BIT 7=1?
F671| 98          F672| 29 EØ        BMI  ADCTST3 ; YES, CONTINUE
F674| FØØ5        BEQ  KEYPLUG
F676| F676        ADCERR .EQU  *
F676| A2 ØD       LDX  #ØD     ; NO,
F678| 2Ø 7BF7      JSR  MESSERR ; PRINT 'A/D' MESS
F67B|
F67B| ; KEYBOARD PLUGIN TEST
F67B| ;
F67B| AD Ø8CØ      KEYPLUG LDA   KEYBD   ; IS KYBD PLUGGED IN?
F67E| ØA          F67F| 1Ø41        ASL   A        ; (IS LIGHT CURRENT
F681| AD DFFF      BPL   SEX      ; PRESENT?) NO, BRANCH
F684| 3Ø3C        LDA   SYSD1   ; IS ERROR FLAG SET?
F686|
F686| ; RECONFIGURE THE SYSTEM
F686| ;
F686| A9 77       RECON  LDA   #77     ; TURN ON SCREEN
F688| 8D DFFF      STA   SYSD1
F68B| 2Ø 98FD      JSR  CLDSTRT ; INITIALIZE MONITOR AND DEFAULT CHARACTER SET
F68E| 2C 1ØCØ      BIT   KBDSTRB ; CLEAR KEYBOARD
F691| AD FFCF      LDA   EXPROM ; DISABLE ALL SLOTS
F694| AD 2ØCØ      LDA   ØCØØ
F697| A9 1Ø       LDA   #1Ø     ; TEST FOR "APPLE 1"
F699| 2D Ø8CØ      AND   KEYBD
F69C| DØØ3        BNE  BOOT    ; NO, DO REGULAR BOOT
F69E| 2Ø Ø1F9      JSR  MONITOR ; AND NEVER COME BACK
F6A1| A2 Ø1       F6A3| 86 87        BOOT  LDX  #Ø1     ; READ BLOCK Ø
F6A5| CA          F6A6| 86 85        STX   IBCMD
F6A8| A9 AØ       F6A8| 85 86        DEX
F6AA| 85 86        STA   IBBUFP ; INTO RAM AT $AØØØ
F6AC| 4A          F6AC| 85 91        LSR   A        ; FOR TRACK 8Ø
F6AD| 85 91        STA   PREVTRK ; MAKE IT RECALIBRATE TOO!
F6AF| 8A          F6BØ| 2Ø 79F4      TXA
F6BØ| 2Ø 79F4      JSR  BLOCKIO
F6B3| 9ØØA        BCC  GOBOOT ; IF WE'VE SUCCEEDED. DO IT UP
F6B5| A2 1C       LDX  #1C
F6B7| 2Ø 38F7      JSR  STRWT
F6BA| 2Ø ØFFD      JSR  KEYIN
F6BD| BØE2        BCS  BOOT
F6BF| 4C ØØAØ      GOBOOT JMP  ØAØØØ ; GO TO IT FOOL...
F6C2|
F6C2| ; SYSTEM EXCERCISER
F6C2| ;
F6C2| AØ 7F       SEX   LDY   #7F     ; TRY FROM
F6C4| 98          F6C5| 29 FE        SEX1  TYA   ; $7F TO Ø
F6C7| 49 4E       F6C9| FØØ3        AND   #ØFE    ; ADD.=_
F6C9| FØØ3        F6CB| B9 ØØCØ      EOR   #4E     ; $4E OR $4F
F6CB| B9 ØØCØ      BEQ  SEX2    ; YES, SKP
F6CE| 88          F6CF| DØF3        LDA   KYBD,Y ; NO, CONT
F6CE| 88          F6CF| DØF3        DEY
F6CF| DØF3        BNE  SEX1    ; NEXT ADD

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F6D1	AD 51C0		LDA	TXTMD	; SET TXT	
F6D4	B9 00C1	SEX3	LDA	SLT1,Y	; EXCERCISE	
F6D7	B9 00C2		LDA	SLT2,Y	; ALL	
F6DA	B9 00C3		LDA	SLT3,Y	; SLOTS	
F6D9	B9 00C4		LDA	SLT4,Y		
F6E0	AD FFCF		LDA	EXPROM	; DISABLE EXPANSION ROM AREA	
F6E3	C8		INY			
F6E4	D0EE		BNE	SEX3		
F6E6		;				
F6E6		;	;	RAM TEST ROUTINE		
F6E6			USRENTRY	LDA	#72+ROM	
F6E8	8D DFFF			STA	SYSD1	
F6EB	A9 18			LDA	#18	
F6ED	8D D0FF			STA	ZPREG	
F6F0	A9 00			LDA	#00	
F6F2	A2 07			LDX	#07	
F6F4	95 10	RAMTST0		STA	ZRPG1,X	
F6F6	CA			DEX		
F6F7	10FB			BPL	RAMTST0	
F6F9	20 84F7			JSR	RAMSET	
F6FC	08			PHP		
F6FD	20 F6F7	RAMTST1		JSR	RAMWT	
F700	20 F6F7			JSR	RAMWT	
F703	28			PLP		
F704	6A			ROR	A	
F705	08			PHP		
F706	20 A1F7			JSR	PTRINC	
F709	D0F2			BNE	RAMTST1	
F70B	20 84F7			JSR	RAMSET	
F70E	08			PHP		
F70F	20 FAF7	RAMTST4		JSR	RAMRD	
F712	48			PHA		
F713	A9 00			LDA	#00	
F715	91 18			STA	(PTRLO),Y	
F717	68			PLA		
F718	28			PLP		
F719	6A			ROR	A	
F71A	08			PHP		
F71B	20 A1F7			JSR	PTRINC	
F71E	D0EF			BNE	RAMTST4	
F720						
F720		;	;	;	;	
F720		;	;	;	;	
F720	A9 00			LDA	#00	
F722	8D EFFF			STA	BNKSW	
F725	8D D0FF			STA	ZPREG	
F728	A2 07			LDX	#07	
F72A	BD 1018	RAMTST6		LDA	PHPR,X	
F72D	95 10			STA	ZRPG1,X	
F72F	CA			DEX		
F730	10F8			BPL	RAMTST6	
F732	20 7EF7			JSR	ERROR	
F735	4C 75F5			JMP	ERRLP	
F738						
F738		;	*****			
F738		;	SARA TEST SUBROUTINES			
F738		;	*****			
F738						
F738	BD CDF4	STRWT	LDA	CHPG,X		
F73B	48		PHA			
F73C	09 80		ORA	#80	; NORMAL VIDEO	
F73E	20 25FC		JSR	COUT	; & PRINT	
F741	E8		INX		; NXT	
F742	68		PLA		; CHR	
F743	10F3		BPL	STRWT		
F745	4C 07FD		JMP	CROUT1	; CLR TO END OF LINE	
F748						
F748		;	SUBROUTINE RAM			
F748		;				
F748	48	RAM	PHA		; SV ACC	
F749	8A		TXA		; CONVRT	
F74A	4A		LSR	A	; ADD TO	
F74B	4A		LSR	A	; USE FOR	
F74C	4A		LSR	A	; 8 ENTRY	
F74D	4A		LSR	A		
F74E	08		PHP			
F74F	4A		LSR	A		
F750	28		PLP			
F751	AA		TAX			
F752	BD C5F4		LDA	RAMTBL,X	; LOOKUP	
F755	1014		BPL	RAM0	; LF VAL	
F757	48		PHA		; <0, GET	
F758	AD EFFF		LDA	BNKSW		
F75B	29 0F		AND	#0F	; WHICH	
F75D	AA		TAX			
F75E	68		PLA			
F75F	E0 00		CPX	#00		
F761	F013		BEQ	RAM1	; BANK?	
F763	4A		LSR	A	; SET	

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F764	4A		LSR	A	; PROPER
F765	4A		LSR	A	; RAM
F766	CA		DEX		; VAL
F767	D00D		BNE	RAM1	
F769	29 05		AND	#05	; CONVERT
F76B	D009	RAMØ	BNE	RAM1	; TO VAL
F76D	8A		TXA		
F76E	F002		BEQ	RAMØ	
F770	A9 03		LDA	#03	
F772	9002	RAMØ	BCC	RAM1	
F774	49 03		EOR	#03	
F776	29 07	RAM1	AND	#07	; BANKSW
F778	AA		TAX		
F779	68		PLA		
F77A	6Ø		RTS		
F77B					;
F77B					; SUBROUTINE ERROR
F77B					;
F77B	2Ø 38F7	MESSERR	JSR	STRWT	; PRINT MESSAGE FIRST
F77E	A9 F3	ERROR	LDA	#ØF2+ROM	; SET 1
F78Ø	8D DFFF		STA	SYSD1	; MHZ MO
F783	6Ø		RTS		
F784					;
F784					; SUBROUTINE RAMSET
F784					;
F784	A2 Ø1	RAMSET	LDX	#Ø1	
F786	86 1A		STX	BNK	
F788	AØ ØØ		LDY	#ØØ	
F78A	A9 AA		LDA	#ØAA	
F78C	38		SEC		
F78D	48	RAMSET1	PHA		
F78E	Ø8		PHP		
F78F	A5 1A		LDA	BNK	
F791	Ø9 ØØ		ORA	#ØØ	
F793	8D 1914		STA	IBNK	
F796	A9 ØØ		LDA	#ØØ	
F798	85 19		STA	PTRHI	
F79A	A2 ØØ		LDX	#ØØ	
F79C	86 18		STX	PTRLO	
F79E	28		PLP		
F79F	68		PLA		
F7AØ	6Ø		RTS		
F7A1					;
F7A1					; SUBROUTINE PTRINC
F7A1					;
F7A1	48	PTRINC	PHA		
F7A2	E6 18		INC	PTRLO	
F7A4	DØ1D		BNE	RETS	
F7A6	A5 1A		LDA	BNK	
F7A8	1ØØE		BPL	PINC1	
F7AA	A5 19		LDA	PTRHI	
F7AC	C9 13		CMP	#13	
F7AE	FØØ6		BEQ	PINC2	
F7BØ	C9 17		CMP	#17	
F7B2	DØØ4		BNE	PINC1	
F7B4	E6 19		INC	PTRHI	
F7B6	E6 19	PINC2	INC	PTRHI	
F7B8	E6 19	PINC1	INC	PTRHI	
F7BA	DØØ7		BNE	RETS	
F7BC	C6 1A		DEC	BNK	
F7BE	C6 1A		DEC	BNK	
F7CØ	2Ø 8DF7		JSR	RAMSET1	
F7C3	68	RETS	PLA		
F7C4	A6 1A		LDX	BNK	
F7C6	EØ FD		CPX	#ØFD	
F7C8	6Ø		RTS		
F7C9					;
F7C9					; SUBROUTINE RAMERR
F7C9					;
F7C9	48	RAMERR	PHA		
F7CA	A6 19		LDX	PTRHI	
F7CC	A4 1A		LDY	BNK	
F7CE	3Ø19		BMI	RAMERR4	
F7DØ	8A		TXA		
F7D1	3Ø1D		BMI	RAMERR5	
F7D3	18		CLC		
F7D4	69 2Ø		ADC	#2Ø	
F7D6	8C EFFF	RAMERR2	STY	BNKSW	
F7D9	AA		TAX		
F7DA	2Ø 48F7	RAMERR3	JSR	RAM	
F7DD	68		PLA		
F7DE	48		PHA		
F7DF	AØ ØØ		LDY	#ØØ	
F7E1	51 18		EOR	(PTRLO), Y	
F7E3	15 1Ø		ORA	ZRPG1, X	
F7E5	95 1Ø		STA	ZRPG1, X	
F7E7	68		PLA		
F7E8	6Ø		RTS		
F7E9	A9 ØØ	RAMERR4	LDA	#ØØ	
F7EB	8D EFFF		STA	BNKSW	

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F7EE| FØEA      BEQ      RAMERR3
F7F0| 38        SEC
F7F1| E9 60      SBC      #60
F7F3| C8        INY
F7F4| DØEØ      BNE      RAMERR2
F7F5|           ;
F7F6|           ; SUBROUTINE RAMWT
F7F6|           ;
F7F6| 49 FF      RAMWT    EOR      #0FF
F7F8| 91 18      STA      (PTRLO),Y
F7FA| D1 18      RAMRD    CMP      (PTRLO),Y
F7FC| DØCB      BNE      RAMERR
F7FE| 60        RET1    RTS
F7FF|           .END

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SYMBOL TABLE DUMP

AB - Absolute	LB - Label	UD - Undefined	MC - Macro
RF - Ref	DF - Def	PR - Proc	FC - Func
PB - Public	PV - Private	CS - Consts	

ACIA	LB F63E	ACIACM	AB CØF2	ACIACN	AB CØF3	ACIAST	AB CØF1	ADCERR	LB F676
ADCTST1	LB F663	ADCTST3	LB F669	ADRS	AB CØ47	ADTO	AB CØ66	ATD	LB F653
BLOCKIO	AB F479	BNK	AB Ø01A	BNKSW	AB FFFEF	BOOT	LB F6A1	CHPG	LB F4CD
CLDSTRT	AB FD98	CNTWR	LB F532	COUT	AB FC25	CROUT1	AB FDØ7	CV	AB ØØ5D
DISK1	LB F513	DISKOFF	AB CØDØ	ERRLP	LB F575	ERRLP1	LB F593	ERROR	LB F77E
EXPROM	AB CF7F	GOBOOT	LB F6BF	GRMD	AB CØ5Ø	IBBUFP	AB ØØ85	IBCMD	AB ØØ87
IBNK	AB 1419	KBDSTRB	AB CØ1Ø	KEYBD	AB CØØ8	KEYIN	AB FDØF	KEYPLUG	LB F67B
KYBD	AB CØØØ	MESERR	LB F77A	MONITOR	AB F9Ø1	NMEM1	LB F54F	NMEM2	LB F562
NOGOOD	LB F52A	NOMEM	LB F548	NXBIT	LB F526	NXBYT	LB F524	PDLEN	AB CØ58
PHPR	AB 181Ø	PINC1	LB F7B8	PINC2	LB F7B6	PREVTRK	AB ØØ91	PTRHI	AB ØØ19
PTRINC	LB F7A1	PTRLO	AB ØØ18	PULBT	LB F53A	RAM	LB F748	RAMØ	LB F76B
RAMØØ	LB F772	RAM1	LB F776	RAMERR	LB F7C9	RAMERR2	LB F7D6	RAMERR3	LB F7DA
RAMERR4	LB F7E9	RAMERR5	LB F7FØ	RAMRD	LB F7FA	RAMSET	LB F784	RAMSET1	LB F78D
RAMTBL	LB F4C5	RAMSTØ	LB F6F4	RAMTST1	LB F6FD	RAMTST4	LB F7ØF	RAMTST6	LB F72A
RAMWT	LB F7F6	RAMWT1	LB F5A1	RAMWT2	LB F5A5	RAMWT4	LB F5AD	RECON	LB F686
RET1	LB F7FE	RETS	LB F7C3	ROM	AB ØØØ1	ROMTST	LB F5E7	ROMTST1	LB F5F2
ROMTST2	LB F5FE	SARATEST	PR ----	SETCVH	AB FBC7	SETUP	AB FD9D	SEX	LB F6C2
SEX1	LB F6C4	SEX2	LB F6CE	SEX3	LB F6D4	SLT1	AB C1ØØ	SLT2	AB C2ØØ
SLT3	AB C3ØØ	SLT4	AB C4ØØ	STKØ	AB ØØFF	STRWT	LB F738	SYSD1	AB FFDF
SYSD2	AB FFD2	SYSD3	AB FFD3	SYSEØ	AB FFEØ	SYSE2	AB FFE2	SYSE3	AB FFE3
TXTMID	AB CØ51	USRENTRY	LB F6E6	VIATST	LB F6ØD	ZP1	LB F5BE	ZP2	LB F5D4
ZP3	LB F5E2	ZPREG	AB FFDØ	ZRPG	AB ØØØØ	ZRPG1	AB ØØ1Ø		

Assembly complete: 545 lines
0 Errors flagged on this Assembly

6502 OPCODE STATIC FREQUENCIES

ADC :	1Ø	*****
AND :	12	*****
ASL :	3	***
BCC :	3	***
BCS :	1	*
BEQ :	12	*****
BIT :	1	m
BMI :	4	***
BNE :	31	*****
BPL :	9	***
CLC :	3	***
CLD :	1	m
CMP :	1Ø	*****
CPX :	5	***
CPY :	2	**
DEC :	3	***
DEX :	9	*****
DEY :	5	***
EOR :	5	***
INC :	6	*****
INX :	6	*****
INY :	6	*****
JMP :	4	***
JSR :	29	*****
LDA :	56	M *****
LDX :	24	*****
LDY :	1Ø	*****
LSR :	9	*****
ORA :	3	***
PHA :	11	*****
PHP :	6	***
PLA :	12	*****
PLP :	4	***
ROR :	2	**
RTS :	6	*****
SBC :	1	m

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SEC :	2	**
STA :	30	*****
STX :	18	*****
STY :	4	****
TAX :	4	****
TAY :	2	**
TXA :	6	*****
TXS :	2	**
TYA :	4	****

Minimum frequency = 1
Maximum frequency = 56

Average frequency = 8

Unused opcodes:

BRK BVC BVS CLI CLV NOP ROL RTI SED SEI TSX

Program opcode usage: 80 %

(1.00) That's all, Folks ...

Source Code Listing

for

Apple ////

ROM - Monitor

David T. Craig
736 Edgewater
Wichita, Kansas 67230

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HD:Apple ///:ROM - Monitor

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```

0000| ;*****  

0000| ;* APPLE /// ROM - MONITOR  

0000| ;* COPYRIGHT 1979 BY APPLE COMPUTER, INC.  

0000| ;*****  

0000| .ABSOLUTE  

0000| .PROC MONITOR  

0000| .ORG 0F7FE  

F7FE| ;  

F7FE| ;  

F7FE| 60 RET1 RTS  

F7FF| 3F .BYTE 03F  

F800| E9 01 SBC #01  

F802| F0FA BEQ RET1  

F804| E9 01 SBC #01  

F806| F0F6 BEQ RET1  

F808| E9 01 SBC #01  

F80A| F0F2 BEQ RET1  

F80C| E9 01 SBC #01  

F80E| F0EE BEQ RET1  

F810| E9 01 SBC #01  

F812| F0EA BEQ RET1  

F814| E9 01 SBC #01  

F816| F0E6 BEQ RET1  

F818| E9 01 SBC #01  

F81A| F0E2 BEQ RET1  

F81C| E9 01 SBC #01  

F81E| F0DE BEQ RET1  

F820| E9 01 SBC #01  

F822| F0DA BEQ RET1  

F824| E9 01 SBC #01  

F826| F0D6 BEQ RET1  

F828| E9 01 SBC #01  

F82A| F0D2 BEQ RET1  

F82C| E9 01 SBC #01  

F82E| F0CE BEQ RET1  

F830| E9 01 SBC #01  

F832| F0CA BEQ RET1  

F834| E9 01 SBC #01  

F836| F0C6 BEQ RET1  

F838| E9 01 SBC #01  

F83A| F0C2 BEQ RET1  

F83C| E9 01 SBC #01  

F83E| F0BE BEQ RET1  

F840| E9 01 SBC #01  

F842| F0BA BEQ RET1  

F844| E9 01 SBC #01  

F846| F0B6 BEQ RET1  

F848| E9 01 SBC #01  

F84A| F0B2 BEQ RET1  

F84C| E9 01 SBC #01  

F84E| F0AE BEQ RET1  

F850| E9 01 SBC #01  

F852| F0AA BEQ RET1  

F854| E9 01 SBC #01  

F856| F0A6 BEQ RET1  

F858| E9 01 SBC #01  

F85A| F0A2 BEQ RET1  

F85C| E9 01 SBC #01  

F85E| F09E BEQ RET1  

F860| E9 01 SBC #01  

F862| F09A BEQ RET1  

F864| E9 01 SBC #01  

F866| F096 BEQ RET1  

F868| E9 01 SBC #01  

F86A| F092 BEQ RET1  

F86C| E9 01 SBC #01  

F86E| F08E BEQ RET1  

F870| E9 01 SBC #01  

F872| F08A BEQ RET1  

F874| E9 01 SBC #01  

F876| F086 BEQ RET1  

F878| E9 01 SBC #01  

F87A| F082 BEQ RET1  

F87C| E9 01 SBC #01  

F87E| F002 BEQ RET3  

F880| E9 01 SBC #01  

F882| F07C BEQ RET2  

F884| E9 01 SBC #01  

F886| F078 BEQ RET2  

F888| E9 01 SBC #01  

F88A| F074 BEQ RET2  

F88C| E9 01 SBC #01  

F88E| F070 BEQ RET2  

F890| E9 01 SBC #01  

F892| F06C BEQ RET2  

F894| E9 01 SBC #01  

F896| F068 BEQ RET2  

F898| E9 01 SBC #01  

F89A| F064 BEQ RET2

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F89C	E9 01	SBC	#01
F89E	F060	BEQ	RET2
F8A0	E9 01	SBC	#01
F8A2	F05C	BEQ	RET2
F8A4	E9 01	SBC	#01
F8A6	F058	BEQ	RET2
F8A8	E9 01	SBC	#01
F8AA	F054	BEQ	RET2
F8AC	E9 01	SBC	#01
F8AE	F050	BEQ	RET2
F8B0	E9 01	SBC	#01
F8B2	F04C	BEQ	RET2
F8B4	E9 01	SBC	#01
F8B6	F048	BEQ	RET2
F8B8	E9 01	SBC	#01
F8BA	F044	BEQ	RET2
F8BC	E9 01	SBC	#01
F8BE	F040	BEQ	RET2
F8C0	E9 01	SBC	#01
F8C2	F03C	BEQ	RET2
F8C4	E9 01	SBC	#01
F8C6	F038	BEQ	RET2
F8C8	E9 01	SBC	#01
F8CA	F034	BEQ	RET2
F8CC	E9 01	SBC	#01
F8CE	F030	BEQ	RET2
F8D0	E9 01	SBC	#01
F8D2	F02C	BEQ	RET2
F8D4	E9 01	SBC	#01
F8D6	F028	BEQ	RET2
F8D8	E9 01	SBC	#01
F8DA	F024	BEQ	RET2
F8DC	E9 01	SBC	#01
F8DE	F020	BEQ	RET2
F8E0	E9 01	SBC	#01
F8E2	F01C	BEQ	RET2
F8E4	E9 01	SBC	#01
F8E6	F018	BEQ	RET2
F8E8	E9 01	SBC	#01
F8EA	F014	BEQ	RET2
F8EC	E9 01	SBC	#01
F8EE	F010	BEQ	RET2
F8F0	E9 01	SBC	#01
F8F2	F00C	BEQ	RET2
F8F4	E9 01	SBC	#01
F8F6	F008	BEQ	RET2
F8F8	E9 01	SBC	#01
F8FA	F004	BEQ	RET2
F8FC	E9 01	SBC	#01
F8FE	F000	BEQ	RET2
F900	60	RET2	RTS
F901	;		
F901	;		
F901	0058	SCRNLOC	.EQU 58
F901	0058	LMARGIN	.EQU SCRNLLOC
F901	0059	RMARGIN	.EQU SCRNLLOC+1
F901	005A	WINTOP	.EQU SCRNLLOC+2
F901	005B	WINBTM	.EQU SCRNLLOC+3
F901	005C	CH	.EQU SCRNLLOC+4
F901	005D	CV	.EQU SCRNLLOC+5
F901	005E	BAS4L	.EQU SCRNLLOC+6
F901	005F	BAS4H	.EQU SCRNLLOC+7
F901	0060	BAS8L	.EQU SCRNLLOC+8
F901	0061	BAS8H	.EQU SCRNLLOC+9
F901	0058	TBAS4L	.EQU SCRNLLOC+A
F901	0063	TBAS4H	.EQU SCRNLLOC+0B
F901	0064	TBAS8L	.EQU SCRNLLOC+0C
F901	0065	TBAS8H	.EQU SCRNLLOC+0D
F901	0066	FORGND	.EQU SCRNLLOC+0E
F901	0067	BKGND	.EQU SCRNLLOC+0F
F901	0068	MODES	.EQU SCRNLLOC+10
F901	0069	CURSOR	.EQU SCRNLLOC+11
F901	006A	STACK	.EQU SCRNLLOC+12
F901	006B	PROMPT	.EQU SCRNLLOC+13
F901	006C	TEMPX	.EQU SCRNLLOC+14
F901	006D	TEMPY	.EQU SCRNLLOC+15
F901	006E	CSWL	.EQU SCRNLLOC+16
F901	006F	CSWH	.EQU SCRNLLOC+17
F901	0070	KSWL	.EQU SCRNLLOC+18
F901	0071	KSWH	.EQU SCRNLLOC+19
F901	0072	PCL	.EQU SCRNLLOC+1A
F901	0073	PCH	.EQU SCRNLLOC+1B
F901	0074	A1L	.EQU SCRNLLOC+1C
F901	0075	A1H	.EQU A1L+1
F901	0076	A2L	.EQU A1L+2
F901	0077	A2H	.EQU A1L+3
F901	0078	A3L	.EQU A1L+4
F901	0079	A3H	.EQU A1L+5
F901	007A	A4L	.EQU A1L+6

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F901	007B	A4H	.EQU	A1L+7
F901	007C	STATE	.EQU	A1L+8
F901	007D	YSAV	.EQU	A1L+9
F901	007E	INBUF	.EQU	A1L+0A
F901	0080	TEMP	.EQU	A1L+0C
F901	0069	MASK	.EQU	CURSOR
F901	;			
F901	C000	KBD	.EQU	0C000
F901	C010	KBDSTRB	.EQU	0C010
F901	;			
F901	0358	USERADR	.EQU	358
F901	F479	BLOCKIO	.EQU	0F479
F901	F686	RECON	.EQU	0F686
F901	F4EE	DIAGN	.EQU	0F4EE
F901	0050	INBUFLEN	.EQU	50
F901	0081	IBSLOT	.EQU	81
F901	0082	IBDRVN	.EQU	IBSLOT+1
F901	0085	IBBUFP	.EQU	IBSLOT+4
F901	0087	IBCMD	.EQU	IBSLOT+6
F901	;			
F901	F901	ENTRY	.EQU	*
F901	BA	TSX		
F902	86 6A	STX	STACK	
F904	D8	MON	CLD	; MUST BE HEX MODE
F905	20 4EFC		JSR	BELL
F908	A6 6A	MONZ	LDX	STACK
F90A	9A		TXS	; RESTORE STACK TO ORIGINAL LOCATION
F90B	A9 DF		LDA	#0DF
F90D	85 6B		STA	PROMPT
F90F	20 D5FC		JSR	GETLNZ
F912	20 67F9	SCAN	JSR	ZSTATE
F915	20 2CF9	NXTINP	JSR	GETNUM
F918	84 7D		STY	YSAV
F91A	A0 12		LDY	#12
F91C	88	CMDSRCH	DEY	; 18 COMMANDS
F91D	30E5		BMI	MON
F91F	D9 6CF9		CMP	CMDTAB, Y
F922	D0F8		BNE	CMDSRCH
F924	20 5EF9		JSR	; NO KEEP LOOKING
F927	A4 7D		TOSUB	; PERFORM FUNCTION
F929	4C 15F9		LDY	YSAV
F92C			JMP	NXTINP
F92C	A2 00			; DO NEXT COMMAND
F92E	86 76	GETNUM	LDX	#00
F930	86 77		STX	A2L
F932	B1 7E	NXTCHR	STX	A2H
F934	C8		LDA	(INBUF), Y
F935	49 B0		INY	
F937	C9 0A		EOR	#0B0
F939	9006		CMP	#0A
F93B	69 88		BCC	; TEST FOR DIGIT
F93D	C9 FA		DIGIT	; SAVE IT IF 1-9
F93F	902A		ADC	#88
F941	A2 03	DIGIT	CMP	#0FA
F943	0A		BCC	; TEST FOR HEX A-F
F944	0A		DIGRET	
F945	0A		LDX	#03
F946	0A		ASL	A
F947	0A	NXTBIT	ASL	A
F948	26 76		A	; SHIFT INDEX FOR NEXT TIME
F94A	26 77		ROL	A2L
F94C	CA		ROL	A2H
F94D	10F8		DEX	
F94F	A5 7C	NXTBAS	BPL	NXTBIT
F951	D006		NXTBAS	STATE
F953	B5 77		BNE	NXTBS2
F955	95 75		LDA	A2H, X
F957	95 79		STA	A1H, X
F959	E8	NXTBS2	STA	A3H, X
F95A	F0F3		INX	
F95C	D0D4		BEQ	NXTBAS
F95E			BNE	NXTCHR
F95E				; SWITCH ROUTINE FOR CHARACTER
F95E	A9 FA	TOSUB	LDA	#0FA
F960	48		PHA	; PUSH ADDRESS OR FUNCTION
F961	B9 7DF9		LDA	; AND RETURN IT
F964	48		CMDVEC, Y	
F965	A5 7C		PHA	
F967	A0 00	ZSTATE	LDA	STATE
F969	84 7C		LDY	#00
F96B	60	DIGRET	STY	STATE
F96C	F96C	CMDTAB	RTS	; RESET STATE OF SCAN
F96C	00		.EQU	*
F96D	03		.BYTE	00
F96E	06		.BYTE	03
F96F	EB		.BYTE	06
F970	EC		.BYTE	0EB
F971	EE		.BYTE	0EC
F972	EF		.BYTE	0EE
			.BYTE	0EF
				; G =GP (CALL) SUBROUTINE
				; J =JUMP (CONT) PROGRAM
				; M =MOVE MEMORY
				; R =READ DISK BLOCK
				; S =MEMORY SEARCH
				; U =USER FUNCTION
				; V =VERIFY MEMORY BLOCKS

"72.PICT" 603 KB 2001-09-10 dpi: 600h x 600v pix: 4663h x 6202v

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HD:Apple ///:ROM - Monitor

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F973 FØ		.BYTE	ØFØ	; W	=WRITE DISK BLOCK
F974 F1		.BYTE	ØF1	; X	=REPEAT COMMAND LINE
F975 99		.BYTE	99	; SP	=SPACE (BYTE SEPARATOR)
F976 9B		.BYTE	9B	; "	=ASCII (HI BIT ON)
F977 AØ		.BYTE	ØAØ	; '	=ASCII (HI BIT OFF)
F978 93		.BYTE	93	; :	=SET STORE MODE
F979 A7		.BYTE	ØA7	; .	=RANGE SEPARATOR
F97A A8		.BYTE	ØA8	; /	=COMMAND SEPARATOR
F97B 95		.BYTE	95	; <	=DEST/SOURCE SEPARATOR
F97C C6		.BYTE	ØC6	; CR	=CARRIAGE RETURN
F97D					
F97D F97D	CMDVEC	.EQU	*		
F97D 9Ø		.BYTE	9Ø	; GO-1	
F97E 8E		.BYTE	8E	; JUMP-1	
F97F 3F		.BYTE	3F	; MOVE-1	
F98Ø D3		.BYTE	ØD3	; READ-1	
F981 Ø8		.BYTE	Ø8	; SEARCH-1	
F982 8B		.BYTE	8B	; USER-1	
F983 4E		.BYTE	4E	; VRFY-1	
F984 D6		.BYTE	ØD6	; WRT-1	
F985 2C		.BYTE	2C	; REPEAT-1	
F986 B7		.BYTE	ØB7	; SPCB-1	
F987 1A		.BYTE	1A	; ASCII-1	
F988 1C		.BYTE	1C	; ASCIIØ-1	
F989 CB		.BYTE	ØCB	; SETMODE-1	
F98A CB		.BYTE	ØCB	; SETMODE-1	
F98B AD		.BYTE	ØAD	; SEP-1	
F98C A4		.BYTE	ØA4	; DEST-1	
F98D 39		.BYTE	39	; CRMON-1	
F98E					
F98E ;					
F98E E6 7A	NXTA4	INC	A4L		; BUMP 16 BIT POINTERS
F99Ø DØØ2		BNE	NXTA1		
F992 E6 7B		INC	A4H		
F994 E6 74	NXTA1	INC	A1L		; BUMP A1
F996 DØØ5		BNE	TSTA1		
F998 E6 75		INC	A1H		
F99A 38		SEC			
F99B FØ1Ø		BEQ	RETA1		; IN CASE OF ROLL OVER
F99D A5 74	TSTA1	LDA	A1L		
F99F 38		SEC			
F9AØ E5 76		SBC	A2L		
F9A2 85 8Ø		STA	TEMP		
F9A4 A5 75		LDA	A1H		
F9A6 E5 77		SBC	A2H		
F9A8 Ø5 8Ø		ORA	TEMP		
F9AA DØØ1		BNE	RETA1		; IF A1 LESS THAN OR EQUAL TO A2
F9AC 18		CLC			; THEN CARRY CLEAR ON RETURN
F9AD 6Ø	RETA1	RTS			
F9AE ;					
F9AE 48	PRBYTE	PHA			; SAVE LOW NIBBLE
F9AF 4A		LSR	A		
F9BØ 4A		LSR	A		; SHIFT HI NIBBLE TO PRINT.
F9B1 4A		LSR	A		
F9B2 4A		LSR	A		
F9B3 2Ø B9F9		JSR	PRHEXZ		
F9B6 68		PLA			
F9B7 29 ØF	PRHEX	AND	#ØF		; STRIP HI NIBBLE
F9B9 Ø9 BØ	PRHEXZ	ORA	#ØBØ		; MAKE IT NUMERIC
F9BØ C9 BA		CMP	#ØBA		; IS IT >'9'
F9BD 9ØØ2		BCC	PRHEXZ		
F9BF 69 Ø6		ADC	#Ø6		; MAKE IT 'A'-'F'
F9C1 4C 39FC	PRHEX2	JMP	COUT		
F9C4 ;					
F9C4 2Ø AEF9	PRBYCOL	JSR	PRBYTE		
F9C7 ;					
F9C7 A9 BA	PRCOLON	LDA	#ØBA		; PRINT A COLON
F9C9 DØF6		BNE	PRHEX2		; BRANCH ALWAYS
F9CB ;					
F9CB A9 Ø7	TST8ØWID	LDA	#Ø7		; ANTICIPATE
F9CD 24 68		BIT	MODES		; TEST FOR 8Ø
F9CF 5ØØ2		BVC	SVMASK		
F9D1 A9 ØF		LDA	#ØF		
F9D3 85 69	SVMASK	STA	MASK		
F9D5 6Ø		RTS			
F9D6 ;					
F9D6 8A	A1PC	TXA			; TEST FOR NEW PC
F9D7 FØØ7		BEQ	OLDPC		
F9D9 B5 74	A1PC1	LDA	A1L,X		
F9DB 95 72		STA	PCL,X		
F9DD CA		DEX			
F9DE 1ØF9		BPL	A1PC1		
F9EØ 6Ø	OLDPC	RTS			
F9E1 ;					
F9E1 85 69	ASCII1	STA	MASK		; SAVE HI BIT STATUS
F9E3 A4 7D	ASCII2	LDY	YSAV		; MOVE ASCII TO MEMORY
F9E5 B1 7E		LDA	(INBUF),Y		
F9E7 E6 7D		INC	YSAV		; BUMP FOR NEXT THING.
F9E9 AØ ØØ		LDY	#ØØ		

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F9EB	C9 A2		CMP	#0A2	; ASCII " ?
F9ED	D005		BNE	ASCII13	; NOPE, CONTINUE.
F9EF	A5 69		LDA	MASK	
F9F1	1032		BPL	BITON	; HE'S CHANGED MODES.
F9F3	60		RTS		
F9F4	C9 A7	ASCII13	CMP	#0A7	; ASCII ' ?
F9F6	D005		BNE	CRCHK	; NO, TEST FOR EOL.
F9F8	A5 69		LDA	MASK	
F9FA	302D		BMI	BITOFF	; CHANGE MODES.
F9FC	60		RTS		
F9FD	C9 8D	CRCHK	CMP	#8D	; END OF LINE?
F9FF	F007		BEQ	ASCDONE	; YES, FINISHED
FA01	25 69		AND	MASK	
FA03	20 C3FA		JSR	STOR1	; GO STORE IT!
FA06	D0DB		BNE	ASCII12	; DO NEXT.
FA08	60	ASCDONE	RTS		
FA09					
FA09	B1 74	SEARCH	LDA	(A1L),Y	; LOAD SEARCH BYTE
FA0B	C5 7A		CMP	A4L	
FA0D	D006		BNE	SRCH1	
FA0F	20 75FA		JSR	PRINTA1	; DUMP MEMORY
FA12	20 EFFC		JSR	CROUT	
FA15	20 94F9	SRCH1	JSR	NXTA1	; INCREMENT POINTER
FA18	90EF		BCC	SEARCH	; CONTINUE SEARCH
FA1A	60		RTS		; RETURN
FA1B					
FA1B					
FA1B	38	ASCII	SEC		; INDICATE HI ON.
FA1C	90		.BYTE	90	; (BCC - NEVER TAKEN)
FA1D	18	ASCII0	CLC		; INDICATE HI OFF
FA1E	AA	CKMDE	TAX		; SAVE STATE
FA1F	86 7C		STX	STATE	; RETAIN STATE
FA21	49 BA		EOR	#0BA	; ARE WE IN STORE MODE?
FA23	D07D		BNE	ERROR	
FA25	A9 FF	BITON	LDA	#0FF	; SET HI BIT UNMASKED
FA27	B0B8		BCS	ASCII11	
FA29	A9 7F		BITOFF	LDA	; MASK HI BIT
FA2B	10B4		BPL	ASCII11	; ALWAYS BRANCHES
FA2D	2C 00C0	REPEAT	BIT	KBD	; REPEAT UNTIL KEYPRESS
FA30	1003		BPL	REPEAT1	
FA32	4C 0FFD		JMP	KEYIN	
FA35	68	REPEAT1	PLA		
FA36	68	LFA36	PLA		
FA37	4C 12F9		JMP	SCAN	
FA3A					
FA3A					
FA3A	20 B4FA	CRMON	JSR	BL1	
FA3D	4C 08F9		JMP	MONZ	
FA40					
FA40					
FA40	20 9DF9	MOVE	JSR	TSTA1	; TEST VALID RANGE
FA43	B05D		BCS	ERROR	
FA45	B1 74	MOVNXT	LDA	(A1L),Y	; COMPARE BYTE FOR BYTE
FA47	91 7A		STA	(A4L),Y	
FA49	20 8EF9		JSR	NXTA4	; BUMP BOTH A1 AND A4
FA4C	90F7		BCC	MOVNXT	
FA4E	60		RTS		; ALL DONE WITH MOVE
FA4F					
FA4F					
FA4F	20 9DF9	VRFY	JSR	TSTA1	; TEST VALID RANGE
FA52	B04E		BCS	ERROR	
FA54	B1 74	VRFY1	LDA	(A1L),Y	; COMPARE BYTE FOR BYTE
FA56	D1 7A		CMP	(A4L),Y	; MATCH?
FA58	F006		BEQ	VRFY2	; YES, DO NEXT.
FA5A	20 66FA		JSR	MISMATCH	; PRINT BOTH BYTES
FA5D	20 EFFC		JSR	CROUT	; GOTO NEWLINE
FA60	20 8EF9	VRFY2	JSR	NXTA4	; BUMP BOTH A1 AND A4
FA63	90EF		BCC	VRFY1	
FA65	60		RTS		; VERIFY DONE.
FA66					
FA66	A5 7B	MISMATCH	LDA	A4H	; PRINT ADDRESS OF A4
FA68	20 AEF9		JSR	PRBYTE	
FA6B	A5 7A		LDA	A4L	
FA6D	20 C4F9		JSR	PRBYCOL	; OUTPUT A COLON FOR SEPARATOR
FA70	B1 7A		LDA	(A4L),Y	; AND THE DATA
FA72	20 84FA		JSR	PRBYTSP	; PRINT THE BYTE AND A SPACE
FA75	20 87FA	PRINTA1	JSR	PRSPC	; LEAD WITH A SPACE
FA78	A5 75		LDA	A1H	; OUTPUT ADDRESS A1
FA7A	20 AEF9		JSR	PRBYTE	
FA7D	A5 74		LDA	A1L	
FA7F	20 C4F9		JSR	PRBYCOL	; SEPARATE WITH A COLON
FA82	B1 74	PRA1BYTE	LDA	(A1L),Y	; PRINT BYTE POINTED TO BY A1
FA84	20 AEF9		PRBYTSP	JSR	
FA87	A9 A0		LDA	PRBYTE	
FA89	4C 39FC	PRSPC	LDA	#0A0	; PRINT A SPACE
FA8C			JMP	COUT	; END VIA OUTPUT ROUTINE.
FA8C	4C 5803	USER	JMP	USERADR	
FA8F					

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FA8F	68	JUMP	PLA			
FA90	68		PLA			; LEAVE STACK WITH NOTHIN' ON IT.
FA91	20 D6F9	GO	JSR	A1PC		; STUFF PROGRAM COUNTER
FA94	6C 7200		JMP	EPCL		; JUMP TO USER PROG.
FA97		;				
FA97	FA97	RWERROR	.EQU	*		; PRINT ERROR NUMBER
FA97	20 AEF9		JSR	PRBYTE		; PRINT THE OFFENDER
FA9A	A9 A1		LDA	#0A1		; FOLLOWED BY A "!"
FA9C	20 39FC		JSR	COUT		
FA9F	20 07FD	ERROR2	JSR	NOSTOP		; OUTPUT A CARRIAGE RETURN (NO STOPST)
FAA2	4C 04F9	ERROR	JMP	MON		
FAA5		;				
FAA5	A5 76	DEST	LDA	A2L		; COPY A2 TO A4 FOR DESTINATION OP
FAA7	85 7A		STA	A4L		
FAA9	A5 77		LDA	A2H		
FAAB	85 7B		STA	A4H		
FAAD	60		RTS			
FAAE		;				
FAAE	20 B8FA	SEP	JSR	SPCE		; SEPARATOR TEST STORE MODE OR DUMP.
FAB1	98		TYA			; ZERO MODE.
FAB2	F01D		BEQ	SETMDZ		; BRANCH ALWAYS
FAB4		;				
FAB4	C6 7D	BL1	DEC	YSAV		; TEST FOR NO LINE
FAB6	F045		BEQ	DUMP8		; IF NO LINE, GIVEM A ROW OF BYTES
FAB8	CA	SPCE	DEX			; TEST IF AFTER ANOTHER SPACE
FAB9	D016		BNE	SETMDZ		
FABB	C9 BA		CMP	#0BA		; STORE MODE?
FABD	D04B		BNE	TSTDUMP		
FABF	85 7C	STOR	STA	STATE		; KEEP IT IN STORE STATE
FAC1	A5 76		LDA	A2L		; GET BYTE TO BE STORED
FAC3	91 78	STOR1	STA	(A3L),Y		; PUT IT IN MEMORY.
FAC5	E6 78		INC	A3L		; BUMP POINTER
FAC7	D002		BNE	DUMMY		
FAC9	E6 79		INC	A3H		
FACB	60	DUMMY	RTS			; ALSO USED FOR '/' TO CLEAR MODE
FACC		;				
FACC	A4 7D	SETMODE	LDY	YSAV		; USE INPUT CHARACTER
FACE	88		DEY			
FACF	B1 7E		LDA	(INBUF),Y		; TO SET MODE
FAD1	85 7C	SETMDZ	STA	STATE		
FAD3	60		RTS			
FAD4		;				
FAD4	A9 01	READ	LDA	#01		; GET DISK COMMAND TO READ
FAD6	2C		.BYTE	2C		; DUMMY BIT TO SKIP 2 BYTES
FAD7	A9 02	WRTE	LDA	#02		; SET DISK COMMAND TO WRITE
FAD9	85 87	SAVCMD	STA	IBCMD		
FADB	A5 74	RWLOOP	LDA	A1L		
FADD	85 85		STA	IBBUFP		; COMMAND FORMAT IS
FADF	A5 75		LDA	A1H		; BLOCKNUMBER <ADDRESS END ADDRESS
FAE1	85 86		STA	IBBUFP+1		
FAE3	A6 7B		LDX	A4H		; SEND BLOCK NUMBER VIA X & A
FAE5	A5 7A		LDA	A4L		
FAE7	78		SEI			; NO INTERRUPTS WHILE IN MONITOR
FAE8	20 79F4		JSR	BLOCKIO		; DO DISKO FEVER
FAEB	B0AA		BCS	RWERROR		; GIVE UP IF ERROR ENCOUNTERED
FAED	E6 7A		INC	A4L		; BUMP BLOCK NUMBER
FAEF	D002		BNE	NOVER		
FAF1	E6 7B		INC	A4H		
FAF3	E6 75	NOVER	INC	A1H		; BUMP RAM ADDRESS BY 512 BYTES
FAF5	E6 75		INC	A1H		
FAF7	20 9DF9		JSR	TSTA1		; TEST FOR FINISHED
FAFA	90DF		BCC	RWLOOP		; NOT DONE, DO NEXT BLOCK
FAFC	60		RTS			
FAFD		;				
FAFD	A5 75	DUMP8	LDA	A1H		
FAFF	85 77		STA	A2H		
FB01	20 CBF9		JSR	TST80WID		; GET WIDTH MASK INTO ACC
FB04	05 74		ORA	A1L		
FB06	85 76		STA	A2L		
FB08	D006		BNE	DUMP0		; BRANCH ALWAYS
FB0A		;				
FB0A	4A	TSTDUMP	LSR	A		; DUMP?
FB0B	B095	ERROR1	BCS	ERROR		
FB0D	20 CBF9	DUMP	JSR	TST80WID		; SET FOR EITHER 80 OR 40 COLUMNS
FB10	A5 74	DUMP0	LDA	A1L		
FB12	85 7A		STA	A4L		
FB14	A5 75		LDA	A1H		
FB16	85 7B		STA	A4H		
FB18	20 9DF9		JSR	TSTA1		; TEST FOR VALID RANGE
FB1B	B0EE		BCS	ERROR1		
FB1D	20 75FA	DUMP1	JSR	PRINTA1		; PRINT ADDRESS AND FIRST BYTE
FB20	20 94F9	DUMP2	JSR	NXTA1		
FB23	B010		BCS	DUMPASC		; END WITH ASCII
FB25	A5 74		LDA	A1L		; TEST END OF LINE
FB27	25 69		AND	MASK		; FOR 40/80 COLUMN
FB29	D005		BNE	DUMP3		
FB2B	20 35FB		JSR	DUMPASC		
FB2E	D0ED		BNE	DUMP1		; BRANCH ALWAYS
FB30	20 82FA	DUMP3	JSR	PRA1BYTE		; GO PRINT NEXT BYTE AND A SPACE
FB33	D0EB		BNE	DUMP2		; ALWAYS (ACC JUST PULLED AS \$A0)

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FB35| ; DUMPASC
FB35| A5 7A LDA A4L ; RESET TO BEGINNING OF LINE
FB37| 85 74 STA A1L
FB39| A5 7B LDA A4H
FB3B| 85 75 STA A1H
FB3D| 20 87FA JSR PRSPC ; PRINT AN EXTRA SPACE
FB40| A0 00 ASC1 LDY #00 ; TO INDEX MEMORY INDIRECT
FB42| B1 74 LDA (A1L),Y
FB44| 09 80 ORA #80 ; SET NORMAL VIDEO
FB46| C9 A0 CMP #0A0 ; TEST FOR CONTROL CHARACTERS
FB48| B002 BCS ASC2 ; OK TO PRINT NON CONTROLS
FB4A| A9 AE LDA #0AE ; OTHERWISE PRINT A SPACE
FB4C| 20 39FC JSR COUT ; PUT IT OUT
FB4F| 20 8EF9 JSR NXTA4 ; BUMP BOTH A1 AND A4
FB52| B006 BCS ASC3 ; FINISHED
FB54| A5 74 LDA A1L ; TEST END OF LINE
FB56| 25 69 AND MASK
FB58| D0E6 BNE ASC1 ; NOT DONE, PRINT NEXT
FB5A| 4C EFFC ASC3 JMP CROUT
FB5D| ; ;
FB5D| ; ;
FB5D| 38 COL80 SEC ; INDICATE 80 COLUMNS
FB5E| AD 53C0 LDA 0C053 ; GOTO 80 COLUMN MODE
FB61| B004 BCS SET80 ; BRANCH ALWAYS
FB63| ; ;
FB63| 18 COL40 CLC ; INDICATE 40 COLUMNS DESIRED
FB64| AD 52C0 LDA 0C052 ; GOTO 40 COLUMN MODE
FB67| A5 68 SET80 LDA MODES
FB69| 09 40 ORA #40 ; ASSUME 80
FB6B| B002 BCS SET80A ; AND BRANCH IF IT IS
FB6D| 29 BF AND #0BF ; BUT FIX FOR 40 IF NOT
FB6F| 85 68 SET80A STA MODES
FB71| 09 7F ORA #7F ; ISOLATE BIT 7
FB73| 29 A0 AND #0A0 ; (BIT 7 SETS NORMAL/INVERSE)
FB75| 85 66 STA FORGND
FB77| B002 BCS SET0B ; AGAIN ASSUMES 80 COLUMNS
FB79| A9 F0 LDA #0F0 ; IF NOT, SET FOR/BACKGROUND COLOR
FB7B| 85 67 SET0B STA BKGND
FB7D| ; ;
FB7D| A5 58 CLSCRN LDA LMARGIN ; SET CURSOR TO TOP LEFT OF WINDOW
FB7F| 85 5C STA CH
FB81| A5 5A LDA WINTOP
FB83| 85 5D STA CV ; NOW DROP INTO CLEAR END OF PAGE
FB85| ; ;
FB85| A5 5C CLEOP LDA CH ; SAVE CURRENT CURSOR POSITION
FB87| 48 PHA
FB88| A5 5D LDA CV
FB8A| 48 PHA
FB8B| 20 C5FB JSR SETCV
FB8E| 20 A2FB CLEOP1 JSR CLEOL ; CLEAR TO END OF FIRST LINE
FB91| A5 58 LDA LMARGIN
FB93| 85 5C STA CH
FB95| 20 DDFB JSR CURDOWN ; GOTO NEXT LINE
FB98| 90F4 BCC CLEOP1
FB9A| 68 PLA
FB9B| A8 TAY
FB9C| 68 PLA ; RESTORE CURSOR POSITION
FB9D| 85 5C STA CH
FB9F| 98 TYA ; GET OLD CV IN ACC AGAIN
FBA0| B023 BCS SETCV ; BRANCH ALWAYS
FBA2| ; ;
FBA2| A5 5C CLEOL LDA CH ; CLEAR TO END OF LINE FIRST
FBA4| 4C 89FC JMP CLEOL1
FBA7| ; ;
FBA7| C9 80 CONTROL CMP #80 ; IF INVERSE
FBA9| 9065 BCC DISPLAYX ; IF CARRIAGE RETURN THEN NEW LINE
FBAB| C9 8D TSTCR CMP #8D
FBAD| D03A BNE TSTBACK
FBAF| 20 A2FB CARRAGE JSR CLEOL
FBB2| 20 D7FB JSR SETCHZ ; FIRST CLEAR TO THE END OF THIS LINE
FBB5| 4C 16FC JMP NXTLIN ; RESET CURSOR AND GOTO NEXT LINE (CARRY IS SET)
FBB8| ; ;
FBB8| A5 5D CURUP LDA CV ; TEST FOR TOP OF SCREEN
FBBA| C6 5D DEC CV ; ANTICIPATE 'NOT' TOP
FBBC| C5 5A CMP WINTOP
FBBE| D002 BNE CURUP1 ; IT'S NOT TOP, CONTINUE
FBC0| A5 5B LDA WINBTM ; WRAP AROUND TO BOTTOM
FBC2| 38 CURUP1 SEC ; DECREMENT BY ONE
FBC3| E9 01 SBC #01
FBC5| 85 5D SETCV STA CV ; SAVE NEW VERTICAL LINE
FBC7| FBC7 BASCALC .EQU *
FBC7| FBC7 CURDN1 .EQU *
FBC7| A5 5D LDA CV ; GET VALUES FOR FIRST PAGE ($400)
FBC9| 104E BPL BASCALC1 ; ALWAYS
FBCB| ; ;
FBCB| 24 68 CURIGHT BIT MODES ; TEST FOR 80 OR 40
FBCD| 7002 BVS RIGHT1
FBCF| E6 5C INC CH
FBD1| E6 5C INC CH ; BUMP CURSOR HORIZONTAL

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"76.PICT" 657 KB 2001-09-10 dpi: 600h x 600v pix: 4615h x 6213v

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FBD3	A5 5C	LDA	CH	; TEST FOR NEW LINE
FBD5	C5 59	CMP	RMARGIN	
FBD7	A5 58	SETCHZ	LDA	LMARGIN ; JUST IN CASE WE HAVE.
FBD9	905D		BCC	CTRLRET
FBD8	85 5C	SETCVH	STA	CH ; CURSOR AT START OF NEXT LINE
FBD9				; DROP INTO CURDOWN FOR WRAP AROUND
FBD9				;
FBD9	E6 5D	CURDOWN	INC	CV ; MOVE CURSOR DOWN ONE LINE
FBD9	A5 5D		LDA	CV ; ANTICIPATE NOT BOTTOM
FBE1	C5 5B		CMP	WINBTM ; TEST FOR BOTTOM
FBE3	90E2		BCC	CURDN1
FBE5	A5 5A		LDA	WINTOP
FBE7	B0DC		BCS	SETCV ; BRANCH ALWAYS
FBE9				;
FBE9	C9 88	TSTBACK	CMP	#88 ; BACKSPACE?
FBE9	D05D		BNE	TSTBELL
FBE9	24 68	CURLEFT	BIT	MODES ; TEST FOR FORTY OR EIGHTY MODE
FBE9	7002		BVS	LEFT80
FBF1	C6 5C		DEC	CH
FBF3	C6 5C	LEFT80	DEC	CH
FBF5	3006		BMI	LEFTUP
FBF7	A5 5C		LDA	CH ; TEST FOR WRAP AROUND
FBF9	C5 58		CMP	LMARGIN
FBF9	103B		BPL	CTRLRET
FBD9	20 B8FB	LEFTUP	JSR	CURUP
FC00	A5 59		LDA	RMARGIN
FC02	85 5C		STA	CH ; SAVE NEW CURSOR POSITION
FC04	D0E7		BNE	CURLEFT ; BRANCH ALWAYS
FC06				;
FC06	C9 A0	COUT2	CMP	#0A0 ; IS IT CONTROL CHARACTER
FC08	909D		BCC	CONTROL
FC0A	24 68		BIT	MODES ; TEST FOR INVERSE
FC0C	3002		BMI	DISPLAYX ; NO PUT IT OUT
FC0E	29 7F		AND	#7F ; STRIP HI BIT
FC10	20 9DFC	DISPLAYX	JSR	DISPLAY
FC13				;
FC13	20 CBFB	INCHORZ	JSR	CURIGHT ; MOVE CURSOR RIGHT
FC16	B043	NXTLIN	BCS	SCROLL ; IT'S BOTTOM, RESET CH=0 AND SCROLL
FC18	60		RTS	; RESET CH ONLY
FC19				;
FC19	08	BASCALC1	PHP	; CALC BASE ADR IN BAS4L,H
FC1A	48		PHA	
FC1B	4A		LSR	A ; FOR GIVEN LINE NO.
FC1C	29 03		AND	#03 ; 0<=LINE NO.<\$17
FC1E	09 04		ORA	#04 ; ARG=000ABCDE, GENERATE
FC20	85 5F		STA	BAS4H ; BAS4H=0000001CD
FC22	49 0C		EOR	#0C
FC24	85 61		STA	BAS8H
FC26	68		PLA	
FC27	29 18		AND	#18 ; AND
FC29	9002		BCC	BSCLC2 ; BAS4L=EABAB000
FC2B	69 7F		ADC	#7F
FC2D	85 5E	BSCLC2	STA	BAS4L
FC2F	0A		ASL	A
FC30	0A		ASL	A
FC31	05 5E		ORA	BAS4L
FC33	85 5E		STA	BAS4L
FC35	85 60		STA	BAS8L ; SAME FOR PAGE 2
FC37	28		PLP	
FC38	60	CTRLRET	RTS	
FC39				;
FC39	48	COUT	PHA	; SAVE CHARACTER
FC3A	84 6D		STY	TEMPY
FC3C	86 6C		STX	TEMPX
FC3E	20 47FC		JSR	COUT1
FC41	A4 6D		LDY	TEMPY
FC43	A6 6C		LDX	TEMPX
FC45	68		PLA	
FC46	60		RTS	
FC47	6C 6E00	COUT1	JMP	@CSWL ; NORMALLY COUT1
FC4A				;
FC4A	C9 87	TSTBELL	CMP	#87 ; BELL?
FC4C	D004		BNE	LNFD ; NO TEST FOR FORM FEED
FC4E	AE 40C0	BELL	LDX	0C040 ; SOUND BELL
FC51	60		RTS	
FC52	C9 8A	LNFD	CMP	#8A ; LINE FEED?
FC54	D0E2		BNE	CTRLRET
FC56	20 DDFB		JSR	CURDOWN ; MOVE CURSOR DOWN A LINE
FC59	90DD		BCC	CTRLRET ; BRANCH IF NO SCROLL NECESSARY.
FC5B				;
FC5B	A5 5A	SCROLL	LDA	WINTOP ; START WITH TOP LINE
FC5D	48		PHA	; SAVE IT FOR NOW
FC5E	20 C5FB		JSR	SETCV ; GET BASCALC FOR THIS LINE
FC61	A2 03	SCRL1	LDX	#03 ; MOVE CURRENT BASCALC AS DESTINATION
FC63	B5 5E	SCRL2	LDA	BAS4L,X
FC65	95 58		STA	TBAS4L,X ; (TEMPORARY BASE ADDR.)
FC67	CA		DEX	
FC68	10F9		BPL	SCRL2
FC6A	68		PLA	; GET DESTINATION LINE
FC6B	18		CLC	

"77.PICT" 613 KB 2001-09-10 dpi: 600h x 600v pix: 4627h x 6189v

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FC6C	69 01	ADC	#01	; CALCULATE SOURCE LINE.
FC6E	C5 5B	CMP	WINBTM	; IS IT THE LAST LINE?
FC70	B015	BCS	LASTLN	; YES, CLEAR IT
FC72	48	PHA		; SAVE AS NEXT DESTINATION LINE
FC73	20 C5FB	JSR	SETCV	; GET BASE ADDR FOR SOURCE LINE
FC76	A5 59	LDA	RMARGIN	; MOVE SOURCE TO DESTINATION
FC78	4A	LSR	A	; DIVIDE BY 2
FC79	A8	TAY		
FC7A	88	SCRL3	DEY	; DONE YET
FC7B	30E4	BMI	SCRL1	; YES, DO NEXT LINE
FC7D	B1 5E	LDA	(BAS4L),Y	
FC7F	91 58	STA	(TBAS4L),Y	
FC81	B1 60	LDA	(BAS8L),Y	
FC83	91 64	STA	(TBAS8L),Y	
FC85	90F3	BCC	SCRL3	; BRANCH ALWAYS
FC87	A5 58	LASTLN	LDA	LMARGIN
FC89	4A	CLEOL1	LSR	; BLANK FILL THE LAST LINE
FC8A	A8		A	; DIVIDE BY 2
FC8B	B004	BCS	CLEOL2	
FC8D	A5 66	LDA	FORGND	; (NORMALLY A SPACE)
FC8F	91 5E	STA	(BAS4L),Y	
FC91	A5 67	CLEOL2	LDA	BKGND
FC93	91 60	STA	(BAS8L),Y	; (IF 80 COLUMNS, ALSO A SPACE)
FC95	C8	INY		
FC96	98	TYA		; TEST FOR END OF LINE
FC97	0A	ASL	A	; MULT BY 2 AGAIN
FC98	C5 59	CMP	RMARGIN	
FC9A	90ED	BCC	CLEOL1	; CONTINUE IF MORE TO DO.
FC9C	60	RTS		; ALL DONE.
FC9D		;		
FC9D	24 68	DISPLAY	BIT	MODES
FC9F	700C	BVS	DSPL80	; STORE THE SINGLE CHARACTERS AND RETURN
FCA1	46 5C	LSR	CH	; INSURE PROPER 40 COLUMN DISPLAY
FCA3	06 5C	ASL	CH	; BY DROPPING BIT 0
FCA5	20 ADFC	JSR	DSPL80	; DISPLAY IN \$400 PAGE.
FCA8	A5 67	LDA	BKGND	; ALSO SET BACKGROUND COLOR
FCAA	91 60	DSPBKND	STA	(BAS8L),Y
FCAC	60		RTS	
FCAD		;		
FCAD	48	DSPL80	PHA	; PRESERVE CHARACTER
FCAE	A5 5C	LDA	CH	; DETERMINE WHICH PAGE
FCB0	4A	LSR	A	
FCB1	A8	TAY		
FCB2	68	PLA		
FCB3	B0F5	BCS	DSPBKND	; BRANCH IF \$900 PAGE
FCB5	91 5E	STA	(BAS4L),Y	
FCB7	60	RTS		
FCB8		;		
FCB8	B1 7E	NOTCR	LDA	(INBUF),Y
FCBA	20 39FC	JSR	COUT	; ECHO CHARACTER
FCBD	C9 88	CMP	#88	; BACKSPACE
FCBF	F01D	BEQ	BKSPCE	
FCC1	C9 98	CMP	#98	; CANCEL?
FCC3	F008	BEQ	CANCEL	
FCC5	E6 80	INC	TEMP	
FCC7	A5 80	LDA	TEMP	
FCC9	C9 50	CMP	#INBUflen	
FCCB	D017	BNE	NXTCHAR	; NO WRAP AROUND ALLOWED.
FCCD	A9 DC	CANCEL	LDA	#0DC
FCCF	20 39FC	JSR	COUT	; OUTPUT BACKSLASH
FCD2	20 EFFC	JSR	CROUT	
FCD5	FC05	GETLNZ	*.EQU	
FCD5	A5 6B	GETLN	LDA	PROMPT
FCD7	20 39FC		JSR	COUT
FCDA	A0 01		LDY	#01
FCDC	84 80	BKSPCE	STY	TEMP
FCDE	A4 80		LDY	TEMP
FCE0	F0F3		BEQ	GETLN
FCE2	C6 80		DEC	TEMP
FCE4	20 60FD	NXTCHAR	JSR	RDCHAR
FCE7	A4 80		LDY	TEMP
FCE9	91 7E		STA	(INBUF),Y
FCEB	C9 8D		CMP	#8D
FCED	D0C9		BNE	NOTCR
FCEF	FCEF	CROUT	*.EQU	
FCEF	2C 00C0		BIT	KBD
FCF2	1013		BPL	NOSTOP
FCF4	20 2EFD		JSR	KEYIN3
FCF7	C9 A0		CMP	#0A0
FCF9	F007		BEQ	STOPLST
FCFB	C9 89		CMP	#89
FCFD	D008		BNE	NOSTOP
FCFF	4C 9FFA		JMP	ERROR2
FD02	AD 00C0	STOPLST	LDA	KBD
FD05	10FB		BPL	STOPLST
FD07	A9 8D		NOSTOP	LDA
FD09	4C 39FC		JMP	#8D
FD0C				COUT
FD0C	6C 7000	RDKEY	JMP	@KSWL
FD0F				;

"78.PICT" 621 KB 2001-09-10 dpi: 600h x 600v pix: 4687h x 6214v

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FD0F A9 7F	KEYIN	LDA #7F	; MAKE SURE FIRST IS CURSOR
FD11 85 63		STA TBAS4H	
FD13 20 88FD		JSR PICK	; GO READ SCREEN
FD16 48	KEYIN1	PHA	; SAVE CHR AT CURSOR POSITION
FD17 20 35FD		JSR KEYWAIT	; TEST FOR KEYPRESS
FD1A B008		BCS KEYIN2	; GO GET IT
FD1C A5 69		LDA CURSOR	; GIVE THEM AN underscore FOR A TIME
FD1E 20 9DFC		JSR DISPLAY	
FD21 20 35FD		JSR KEYWAIT	; GO SEE IF KEYPRESSED
FD24 68	KEYIN2	PLA	
FD25 08		PHP	; SAVE KEYPRESS STATUS
FD26 48		PHA	
FD27 20 9DFC		JSR DISPLAY	
FD2A 68		PLA	
FD2B 28		PLP	
FD2C 90E8		BCC KEYIN1	
FD2E AD 00C0	KEYIN3	LDA KBD	; READ KEYBOARD
FD31 2C 10C0	KEYIN4	BIT KBDSTRB	; CLEAR KEYBOARD STROBE
FD34 60		RTS	
FD35 E6 58	KEYWAIT	INC TBAS4L	; JUST KEEP COUNTING
FD37 D009		BNE KWAIT2	
FD39 E6 63		INC TBAS4H	
FD3B A9 7F		LDA #7F	; TEST FOR DONE
FD3D 18		CLC	
FD3E 25 63		AND TBAS4H	
FD40 F005		BEQ KEYRET	; RETURN IF TIMED OUT
FD42 0E 00C0	KWAIT2	ASL KBD	
FD45 90EE		BCC KEYWAIT	
FD47 60	KEYRET	RTS	
FD48 ;			
FD48 FD48	ESC3	.EQU *	
FD48 20 77FD		JSR GOESC	
FD4B A5 68	ESCAPE	LDA MODES	; SET TO + SIGN FOR CURSOR MOVES
FD4D 29 80		AND #80	
FD4F 49 AB		EOR #0AB	
FD51 85 69		STA CURSOR	
FD53 20 0CFD	ESC1	JSR RDKEY	; READ NEXT CHARACTER
FD56 A0 08		LDY #08	; TEST FOR ESCAPE COMMAND
FD58 D9 F0FF	ESC2	CMP ESCTABL, Y	
FD5B F0EB		BEQ ESC3	
FD5D 88		DEY	
FD5E 10F8		BPL ESC2	; LOOP TIL FOUND OR DONE
FD60 ;	RDCHAR	LDA #80	; GO READ A CHARACTER
FD62 25 68		AND MODES	
FD64 85 69		STA CURSOR	; SAVE STANDARD CURSOR
FD66 20 0CFD		JSR RDKEY	
FD69 C9 9B		CMP #9B	; ESCAPE CHARACTER?
FD6B F0DE		BEQ ESCAPE	
FD6D C9 95		CMP #95	; FORWARD COPY?
FD6F D0D6		BNE KEYRET	
FD71 20 88FD		JSR PICK	; GET CHARACTER FROM SCREEN
FD74 09 80		ORA #80	; SET TO NORMAL ASCII
FD76 60		RTS	
FD77 ;			
FD77 A9 FB	GOESC	LDA #0FB	
FD79 48		PHA	
FD7A B9 7FFD		LDA ESCVECT, Y	
FD7D 48		PHA	
FD7E 60		RTS	
FD7F A1	ESCVECT	.BYTE 0A1	; CLEOL-1
FD80 84		.BYTE 84	; CLEOP-1
FD81 7C		.BYTE 7C	; CLSCRN-1
FD82 62		.BYTE 62	; COL40-1
FD83 5C		.BYTE 5C	; COL80-1
FD84 EC		.BYTE 0EC	; CURLEFT-1
FD85 CA		.BYTE 0CA	; CURRIGHT-1
FD86 DC		.BYTE 0DC	; CURDOWN-1
FD87 B7		.BYTE 0B7	; CURUP-1
FD88 ;			
FD88 A5 5C	PICK	LDA CH	; GET A CHARACTER AT CURRENT CURSOR POSITION
FD88 4A		LSR A	; DETERMINE WHICH PAGE.
FD8B A8		TAY	
FD8C 24 68		BIT MODES	; AND IF 80 COLUMN MODE
FD8E 5005		BVC PICK40	; FORGET CARRY IF 40 COLUMNS
FD90 9003		BCC PICK40	; GET CHARACTER FROM \$400
FD92 B1 60		LDA (BAS8L), Y	
FD94 60		RTS	
FD95 B1 5E	PICK40	LDA (BAS4L), Y	
FD97 60		RTS	
FD98 ;			
FD98 FD98	CLDSTRT	.EQU *	
FD98 A9 03		LDA #03	
FD9A 8D D0FF		STA 0FFD0	; ZERO PAGE IS ON 3!
FD9D FD9D	SETUP	.EQU *	
FD9D D8		CLD	; OF COURSE!
FD9E A2 03		LDX #03	
FDA0 86 7F		STX INBUF+1	
FDA2 BD BCFF	SETUP1	LDA NMIRQ, X	

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FDA5	9D CAFF	STA	0FFCA, X
FDA8	BD B4FF	LDA	HOOKS, X
FDAB	95 6E	STA	CSWL, X
FDAD	BD B8FF	LDA	VBOUNDS, X
FDB0	95 58	STA	LMARGIN, X
FDB2	CA	DEX	
FDB3	10ED	BPL	SETUP1
FDB5	85 82	STA	IBDRVN
FDB7	A9 A0	LDA	#0A0 ; INPUT BUFFER AT \$3A0
FDB9	85 7E	STA	INBUF
FDBB	A9 60	LDA	#60
FDBD	85 81	STA	IBSLOT
FDBF	A9 FF	LDA	#0FF
FDC1	85 68	STA	MODES
FDC3	20 63FB	JSR	COL0 ; SET 40 COLUMNS, CLEAR SCREEN
FDC6	;	ADR	.EQU 0A0
FDC6	00A0	CPORTL	.EQU ADR
FDC6	00A1	CPORTR	.EQU ADR+1
FDC6	00A2	CTEMP	.EQU ADR+2
FDC6	00A3	CTEMP1	.EQU ADR+3
FDC6	00A4	YTEMP	.EQU ADR+4
FDC6	00C0	ROWTEMP	.EQU ADR+20
FDC6	C0DB	CWRTON	.EQU 0C0DB
FDC6	C0DA	CWRTOFF	.EQU 0C0DA
FDC6	FFEC	CB2CTRL	.EQU 0FFEC
FDC6	FFED	CB2INT	.EQU 0FFED
FDC6	;		
FDC6	;		
FDC6	A9 78	GENENTR	LDA #78 ; INIT SCREEN INDEX LOCATIONS
FDC8	85 A0	STA	CPORTL
FDCA	A9 08	LDA	#08
FDCC	85 A1	STA	CPORTR
FDCE	A9 F0	LDA	#0F0 ; SET UP INDEX TO CHRSET
FDD0	85 A4	STA	YTEMP
FDD2	A9 00	LDA	#00
FDD4	AA	TAX	
FDD5	95 C0	ZIPTEMPS	STA ROWTEMP, X
FDD7	E8	INX	
FDD8	E0 20	CPX	#20
FDDA	D0F9	BNE	ZIPTEMPS
FDDC	A9 05	LDA	#05 ; FAKE THE FIRST BIT PATTERN
FDDC	18	CLC	; (PHANTOM 9TH BIT SHIFTED AS BIT 0)
FDDF	08	PHP	
FDE0	48	PHA	
FDE1	86 A2	GENASC	STX CTEMP ; GENERATE THE ASCII
FDE3	A0 07	GASC11	LDY #07 ; CODES FOR THE FIRST PASS
FDE5	A6 A2	GASC12	LDX CTEMP
FDE7	8A	GASC13	TXA
FDE8	91 A0	STA	(CPORTL), Y ; \$XXF=CHR 0 / 4
FDEA	E8	INX	; \$XXE=CHR 1 / 5
FDEB	88	DEY	; \$XXD=CHR 2 / 6
FDEC	3006	BMI	GASC14 ; \$XXC=CHR 3 / 7
FDEE	C0 03	CPY	#03 ; \$XXB=CHR 0 / 4
FDF0	D0F5	BNE	GASC13 ; \$XXA=CHR 1 / 5
FDF2	F0F1	BEQ	GASC12 ; \$XX9=CHR 2 / 6
FDF4	20 99FE	GASC14	JSR NXTPORT ; GO DECODE CHARACTER TABLE
FDF7	B008	BCS	CBYTES
FDF9	C9 0A	CMP	#0A ; GO DECODE CHARACTER TABLE
FDFB	D0E6	BNE	GASC11
FDFD	A2 24	LDX	#24
FDFF	D0E0	BNE	GENASC ; SECOND SET OF 4?
FE01	68	CBYTES	PLA
FE02	28	PLP	
FE03	A2 17	LDX	#17 ; (4 CHARACTERS OF 6 ROWS)
FE05	A0 05	CCOLMS	LDY #05 ; (FIVE COLUMNS)
FE07	36 C4	CSHFT	ROL ROWTEMP+4, X ; BREAK BYTE INTO
FE09	0A	ASL	A 5 BIT GROUPS
FE0A	D00E	BNE	SHFTCNT ; BRANCH IF MORE BITS IN THIS BYTE
FE0C	84 A2	STY	CTEMP
FE0E	C6 A4	DEC	YTEMP ; (NOTE. CARRY IS SET)
FE10	F016	BEQ	DONE ; BRANCH IF ALL DONE
FE12	A4 A4	LDY	YTEMP ; GET CHARACTER TABLE INDEX
FE14	B9 C4FE	LDA	CHRSET-1, Y
FE17	2A	ROL	A ; (CARRY KEEPS BYTE NON-ZERO UNTIL ALL 8 ARE
FE18			ARE SHIFTED)
FE18	A4 A2	LDY	CTEMP ; RESTORE COLUMN COUNT
FE1A	88	DEY	; GOT ALL FIVE BITS?
FE1B	D0EA	BNE	CSHFT ; NO, DO NEXT
FE1D	CA	DEX	; ALL ROWS DONE
FE1E	10E5	BPL	CCOLMS ; NO, DO NEXT
FE20	08	PHP	; SAVE REMAINING BIT PATTERN AND CARRY
FE21	48	PHA	
FE22	20 28FE	JSR	STORCHR ; MOVE EM TO NON DISPLAYED VIDEO AREA
FE25	4C 01FE	JMP	CBYTES
FE28		DONE	.EQU *
FE28	FE28		
FE28			
FE28	A2 1F	STORCHR	LDX #1F ; MOVE CHARACTER PATTERNS TO VIDEO AREA
FE2A	A0 00	STORSET	LDY #00

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FE2C B5 C0	STOROW	LDA	ROWTEMP, X	
FE2E 0A		ASL	A	; SHIFT TO CENTER
FE2F 29 3E		AND	#3E	; STRIP EXTRA GARBAGE
FE31 91 A0		STA	(CPORL), Y	
FE33 CA		DEX		
FE34 C8		INY		
FE35 C0 08		CPY	#08	; THIS GROUP DONE
FE37 D0F3		BNE	STOROW	; NO, NEXT ROW
FE39 20 99FE		JSR	NXTPORT	
FE3C C9 08		CMP	#08	
FE3E F004		BEQ	GENDONE	; ALL ROWS STORED?
FE40 8A		TXA		
FE41 10E7		BPL	STORSET	
FE43 60		RTS		; PARTIAL SET (\$478-\$5FF)
FE44 ;	GENDONE	LDA	#01	; SET NORMAL MODE
FE46 85 A2		STA	CTEMP	
FE48 A9 60	GEN1	LDA	#60	; PREPARE TO SEND BYTES TO CHARACTER
FE4A 2C DBC0		BIT	CWRTON	; GENERATOR RAM
FE4D 20 AEFE		JSR	VRETRCE	; WAIT FOR NEXT VERTICAL RETRACE
FE50 A9 20		LDA	#20	; WAIT AGAIN
FE52 20 AEFE		JSR	VRETRCE	
FE55 2C DAC0		BIT	CWRTOFF	; CHARACTERS ARE NOW LOADED
FE58 20 88FE		JSR	ALTCHR	; REPEAT THIS SET FOR OTHER 64 CHARACTERS
FE5B C6 A2		DEC	CTEMP	; HAVE WE DONE ALTERNATES YET?
FE5D 1016		BPL	GEN2	; NO, DO IT!
FE5F A9 08		LDA	#08	; BUMP ASCII VALUES FOR NEXT SET
FE61 85 A1		STA	CPORTH	
FE63 A0 07	NXTASCII	LDY	#07	; THE USUAL COUNTDOWN
FE65 B1 A0	NXTASC2	LDA	(CPORL), Y	
FE67 18		CLC		
FE68 69 08		ADC	#08	
FE6A 91 A0		STA	(CPORL), Y	
FE6C 88		DEY		
FE6D 10F6		BPL	NXTASC2	
FE6F 20 99FE		JSR	NXTPORT	
FE72 90EF		BCC	NXTASCII	
FE74 60		RTS		
FE75 A0 03	GEN2	LDY	#03	; SETUP ALTERNATE WITH UNDERLINES
FE77 A9 7F		LDA	#7F	
FE79 99 FC05	UNDER	STA	05FC, Y	
FE7C 99 FC07		STA	07FC, Y	
FE7F 88		DEY		
FE80 10F7		BPL	UNDER	
FE82 A9 08		LDA	#08	
FE84 85 A1		STA	CPORTH	
FE86 D0C0		BNE	GEN1	
FE88 ;	ALTCHR	LDY	#07	; ADJUST ASCII FOR ALTERNATE SET
FE8A B1 A0	ALTC1	LDA	(CPORL), Y	
FE8C 49 20		EOR	#20	; \$20--> \$40-->\$60
FE8E 91 A0		STA	(CPORL), Y	
FE90 88		DEY		
FE91 10F7		BPL	ALTC1	; ADJUST THEM ALL
FE93 20 99FE		JSR	NXTPORT	
FE96 90F0		BCC	ALTCHR	
FE98 60		RTS		
FE99 ;	NXTPORT	LDA	CPORL	; CONVERT \$78->\$F8 OR \$F8-\$78
FE9B 49 80		EOR	#80	
FE9D 85 A0		STA	CPORL	
FE9F 3002		BMI	NOHIGH	
FEA1 E6 A1		INC	CPORTH	
FEA3 A5 A1	NOHIGH	LDA	CPORTH	
FEA5 C9 0C		CMP	#0C	
FEA7 D004		BNE	PORTDN	
FEA9 A9 04		LDA	#04	
FEAB 85 A1		STA	CPORTH	
FEAD 60	PORTDN	RTS		
FEAE ;	VRETRCE	STA	CTEMP1	; SAVE BITS TO BE STORED
FEB0 AD ECFF		LDA	CB2CTRL	; CONTROL PORT FOR 'CB2'
FEB3 29 3F		AND	#3F	; RESET HI BITS TO 0
FEB5 05 A3		ORA	CTEMP1	
FEB7 8D ECFF		STA	CB2CTRL	
FEBA A9 08		LDA	#08	; TEST VERTICAL RETRACE
FEBC 8D EDFF		STA	CB2INT	
FEBF 2C EDFF	VWAIT	BIT	CB2INT	; WAIT FOR RETRACE
FEC2 F0FB		BEQ	VWAIT	
FEC4 60		RTS		
FEC5 ;	CHRSET	.EQU	*	
FEC5 FEC5		.BYTE	0F0,01,82,18,40,84,81,2F,58,44,81,29,02,1E,01,91,7C,1F,49,30	
FEC5 FEC5		.BYTE	2F,58,44,81,29,02,1E	
FED3 01 91 7C 1F 49 30		.BYTE	8A,08,43,14,31,2A,22,13,0E3,0F7,0C4,91,48,0A2,0DA,24,0C6,4A	
FED9 8A 08 43 14 31 2A 22		.BYTE	62,8C,24,0C6,0F8,63,8C,0C1,46,17,52,8A,0AF,16,14,0E3,33,31	
FEE0 13 E3 F7 C4 91 48 A2		.BYTE		
FEE7 DA 24 C6 4A		.BYTE		
FEEB 62 8C 24 C6 F8 63 8C		.BYTE		

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FFEF2| C1 46 17 52 8A AF 16
FFEF9| 14 E3 33 31
FFED| C6 F8 DC 73 3F 46 17
FF04| 62 8C 21 E6 18 6A 8D
FF0B| 61 CF 18 62
FF2F| 74 D1 B9 18 49 4C 91
FF16| C0 F3 09 2C 91 C0 14
FF1D| 1D 8C EF 07
FF21| 17 43 88 31 84 1E DF
FF28| 0B 31 84 F8 FE 77 3E
FF2F| 3E 17 62 8C FD
FF34| C7 50 E3 0B 51 C5 E8
FF3B| C8 73 18 0C 42 3E 01
FF42| 02 20 42 3E
FF46| 41 18 8C 08 00 70 EE
FF4D| 00 11 11 21 11 02 E0
FF54| 3C 21 31 02 E0
FF59| 1C 00 C8 B9 00 62 14
FF60| 1F 46 A2 DE 43 2C 04
FF67| 88 BE FF CE
FF6B| 7D 37 49 88 95 18 98
FF72| 09 62 D1 44 E8 88 FB
FF79| 02 90 40 00 10
FF7E| E0 03 00 00 40 00 00
FF85| 08 00 00 28 10 42 44
FF8C| 25 82 B8 2F 48
FF91| 25 44 10 82 02 00 2F
FF98| 5A 40 45 02 8E 64 50
FF9F| 90 01 3E 26 42 80
FFA5| 21 80 00 05 00 F8 80
FFAC| 00 05 08 F8 80 28 05
FFB3| 88
FFB4| ;
FFB4| FFB4 HOOKS EQU *
FFB4| 06FC
FFB6| 0FFD WORD COUT2
FFB8| FFB8 VBOUNDS WORD KEYIN
FFB8| 00 50 00 18 EQU *
FFBC| 4C 86F6 NMIRQ JMP RECON ; IN DIAGNOSTICS
FFBF| 40 RTI
FFC0| 43 4F 50 59 52 49 47
FFC7| 48 54 20 4A 41 4E 55
FFCE| 41 52 59 2C 20 31 39
FFD5| 38 30 20 20 41 50 50
FFDC| 4C 45 20 43 4F 4D 50
FFEA| 55 54 45 52 20 49 4E
FFEA| 43 2E 2E 4A 52 48
FFF0| CC D0 D3 B4 B8 88 95 ESCTABL BYTE 0CC, 0D0, 0D3, 0B4, 0B8, 88, 95, 8A, 8B, 00
FFF7| 8A 8B 00
FFFA| ;
FFFA| CAFF NMI WORD 0FFCA
FFFC| EEF4 RESET WORD DIAGN ; NOTHING
FFFE| CDFF IRQ WORD 0FFCD
0000| 0000 END

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SYMBOL TABLE DUMP

AB - Absolute	LB - Label	UD - Undefined	MC - Macro						
RF - Ref	DF - Def	PR - Proc	FC - Func						
PB - Public	PV - Private	CS - Consts							
A1H	AB 0075	A1L	AB 0074	A1PC	LB F9D6	A1PC1	LB F9D9	A2H	AB 0077
A2L	AB 0076	A3H	AB 0079	A3L	AB 0078	A4H	AB 007B	A4L	AB 007A
ADR	AB 00A0	ALTC1	LB FE8A	ALTCHR	LB FE88	ASC1	LB FB40	ASC2	LB FB4C
ASC3	LB FB5A	ASCDONE	LB FA08	ASCII	LB FA1B	ASCII0	LB FA1D	ASCII1	LB F9E1
ASCII2	LB F9E3	ASCII3	LB F9F4	BAS48	AB 005F	BAS4L	AB 005E	BAS8H	AB 0061
BAS8L	AB 0060	BASCALC	LB FBC7	BASCALC1	LB FC19	BELL	LB FC4E	BITOFF	LB FA29
BITON	LB FA25	BKGND	AB 0067	BKSPCE	LB FCDE	BL1	LB FAB4	BLOCKIO	AB F479
BSCLC2	LB FC2D	CANCEL	LB FCCD	CARRAGE	LB FBAF	CB2CTRL	AB FFEC	CB2INT	AB FFED
CBYTES	LB FE01	CCOLMS	LB FE05	CH	AB 005C	CHRSET	LB FEC5	CKMDE	LB FA1E
CLDSTRT	LB FD98	CLEOL	LB FBA2	CLEOL1	LB FC89	CLEOL2	LB FC91	CLEOP	LB FB85
CLEOP1	LB FB8E	CLSCRN	LB FB7D	CMDSRCH	LB F91C	CMDTAB	LB F96C	CMDVEC	LB F97D
COL40	LB FB63	COL80	LB FB5D	CONTROL	LB FBA7	COUT	LB FC39	COUT1	LB FC47
COUT2	LB FC06	CPORTH	AB 00A1	CPORTL	AB 00A0	CRCHK	LB F9FD	CRMON	LB FA3A
CROUT	LB FC07	CSHFTT	LB FE07	CSWH	AB 006F	CSWL	AB 006E	CTEMP	AB 00A2
CTEMP1	AB 00A3	CTRIRET	LB FC38	CURDN1	LB FBC7	CURDOWN	LB FBDD	CURIGHT	LB FBCB
CURLEFT	LB FBED	CURSOR	AB 0069	CURUP	LB FB88	CURUP1	LB FBC2	CV	AB 005D
CWRTOFF	AB C0DA	CWRTON	AB C0DB	DEST	LB FAA5	DIAGN	AB F4EE	DIGIT	LB F941
DIGRET	LB F96B	DISPLAY	LB FC9D	DISPLAYX	LB FC10	DONE	LB FE28	DSPBKND	LB FCAA
DSPL80	LB FCAD	DUMMY	LB FACB	DUMP	LB FB9D	DUMP0	LB FB10	DUMP1	LB FB1D
DUMP2	LB FB20	DUMP3	LB FB30	DUMP8	LB FAFD	DUMPASC	LB FB35	ENTRY	LB F901
ERROR	LB FAA2	ERROR1	LB FB0B	ERROR2	LB FA9F	ESC1	LB FD53	ESC2	LB FD58
ESC3	LB FD48	ESCAPE	LB FD4B	ESCTABL	LB FFFF	ESCVECT	LB FD7F	FORGND	AB 0066
GASC11	LB FDE3	GASC12	LB FDE5	GASC13	LB FDE7	GASC14	LB FDF4	GEN1	LB FE48

"82.PICT" 886 KB 2001-09-10 dpi: 600h x 600v pix: 4687h x 6190v

J. R. Huston
(also worked
on SOS)

J=James
R=Richard
aka
Dick
Huston

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GEN2	LB FE75	GENASC	LB FDE1	GENDONE	LB FE44	GENENTR	LB FDC6	GETLN	LB FCD5
GETLNZ	LB FCD5	GETNUM	LB F92C	GO	LB FA91	GOESC	LB FD77	HOOKS	LB FFB4
IBBUFP	AB 0085	IBCMD	AB 0087	IBDRVN	AB 0082	IBSLOT	AB 0081	INBUF	AB 007B
INBUFLEN	AB 0050	INCHORZ	LB FC13	IRQ	LB FFFE	JUMP	LB FA8F	KBD	AB C000
KBDSTRB	AB C010	KEYIN	LB FD0F	KEYIN1	LB FD16	KEYIN2	LB FD24	KEYIN3	LB FD2E
KEYIN4	LB FD31	KEYRET	LB FD47	KEYWAIT	LB FD35	KSWH	AB 0071	KSWL	AB 0070
KWAIT2	LB FD42	LASTLN	LB FC87	LEFT80	LB FBF3	LEFTUP	LB FBFD	LFA36	LB FA36
LMARGIN	AB 0058	LNFD	LB FC52	MASK	AB 0069	MISMATCH	LB FA66	MODES	AB 0068
MON	LB F904	MONITOR	PR ----	MONZ	LB F908	MOVE	LB FA40	MOVNXT	LB FA45
NMI	LB FFFA	NMIRO	LB FFBC	NOHIGH	LB FE43	NOSTOP	LB FD07	NOTCR	LB FCB8
NOVER	LB FAF3	NXTA1	LB F994	NXTA4	LB F98E	NXTASC2	LB FE65	NXTASCI	LB FE63
NXTBAS	LB F94F	NXTBIT	LB F947	NXTBS2	LB F959	NXTCHAR	LB FCE4	NXTCHR	LB F932
NXTINP	LB F915	NXTLIN	LB FC16	NXTPORT	LB FE99	OLDPC	LB F9E0	PCH	AB 0073
PCL	AB 0072	PICK	LB FD88	PICK40	LB FD95	PORTDN	LB FEAD	PRA1BYTE	LB FA82
PRBYCOL	LB FC94	PRBYTE	LB F9AE	PRBYTSP	LB FA84	PRCOLON	LB F9C7	PRHEX	LB F987
PRHDX2	LB F9C1	PRHDXZ	LB F9B9	PRINTA1	LB FA75	PROMPT	AB 006B	PRSPC	LB FA87
RDCCHAR	LB FD60	RDKEY	LB FD0C	READ	LB FAD4	RECON	AB F686	REPEAT	LB FA2D
REPEAT1	LB FA35	RESET	LB FFBC	RET1	LB F7FE	RET2	LB F900	RET3	LB F882
RETA1	LB F94D	RIGHT1	LB FD01	RMARGIN	AB 0059	ROWTEMP	AB 00C0	RWERROR	LB FA97
RWLOOP	LB FADB	SAVCMD	LB FD09	SCAN	LB F912	SCRLL1	LB FC61	SCRL2	LB FC63
SCRLL3	LB FC7A	SCRNLOC	AB 0058	SCROLL	LB FC5B	SEARCH	LB FA69	SEP	LB FAAE
SET80	LB FB67	SET80A	LB FB6F	SET80B	LB FB7B	SETCHZ	LB FB07	SETCV	LB FB05
SETCVH	LB FBDB	SETMDZ	LB FA01	SETMODE	LB FACC	SETUP	LB FD9D	SETUP1	LB FDA2
SHFTCNT	LB FE1A	SPCE	LB FA08	SRCH1	LB FA15	STACK	AB 006A	STATE	AB 007C
STOPLST	LB FD02	STOR	LB FABF	STOR1	LB FAC3	STORCRHS	LB FE28	STOROW	LB FE2C
STORSET	LB FE2A	SVMASK	LB F9D3	TBAS4H	AB 0063	TBAS4L	AB 0058	TBAS8H	AB 0065
TBAS8L	AB 0064	TEMP	AB 0080	TEMpx	AB 006C	TEMPY	AB 006D	TOSUB	LB F95E
TST80WID	LB F9CB	TSTA1	LB F99D	TSTBACK	LB FBE9	TSTBELL	LB FC4A	TSTCR	LB FBAB
TSTDUMP	LB FE0A	UNDER	LB FE79	USER	LB FA8C	USERADR	AB 0358	VBOUNDS	LB FFB8
VRETRCE	LB FEA8	VRFY	LB FA4F	VRFY1	LB FA54	VRFY2	LB FA60	VWAIT	LB FEBF
WINBTM	AB 005B	WINTOP	AB 005A	WRTE	LB FAD7	YSAV	AB 007D	YTEMP	AB 00A4
ZIPTEMPS	LB FDD5	ZSTATE	LB F967						

Assembly complete: 1129 lines
 Ø Errors flagged on this Assembly

6502 OPCODE STATIC FREQUENCIES

ADC :	5	***
AND :	14	*****
ASL :	12	*****
BCC :	21	*****
BCS :	20	*****
BEQ :	82	*****
BIT :	12	*****
BMI :	7	****
BNE :	41	*****
BPL :	18	*****
BVC :	2	*
BVS :	3	*
CLC :	7	****
CLD :	2	*
CMP :	35	*****
CPX :	1	m
CPY :	2	*
DEC :	7	****
DEX :	7	****
DEY :	9	****
EOR :	6	***
INC :	18	*****
INX :	3	*
INY :	3	*
JMP :	18	*****
JSR :	79	*****
LDA :	117	M
LDX :	12	*****
LDY :	20	*****
LSR :	11	*****
ORA :	10	*****
PHA :	16	*****
PHP :	4	**
PLA :	14	*****
PLP :	3	*
ROL :	4	**
RTI :	1	m
RTS :	34	*****
SBC :	67	*****
SEC :	5	***
SEI :	1	m
STA :	72	*****
STX :	7	***
STY :	5	**
TAX :	2	*
TAY :	5	***
TSX :	1	m
TXA :	2	*
TXS :	1	m
TYA :	3	*

10/31/89 10:04

HD:Apple ///:ROM - Monitor

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Minimum frequency = 1
Maximum frequency = 117

Average frequency = 17

Unused opcodes:

BRK CLI CLV NOP ROR SED

Program opcode usage: 89 %

(1.00) That's all, Folks ...

=FIN/S=

DEC 1997

APPLE /// REV 1 ROM HEX DUMP

Source

ROM file as found with Chris Smolinski's Macintosh SARA emulator application

This hex dump, which was produced by the Apple Macintosh MPW DumpFile tool, lists the Apple /// Revision 1 ROM. This 4KB ROM occupies addresses \$F000-\$FFFF.

File : ROM
Data Fork Length : 4096
Resource Fork Length : 2670

Printed: Dec. 1997 / David T. Craig

Dumping Data Fork from offset 0 to 4095

0: A0 01 A6 81 84 94 A9 05 85 8F 08 68 6A 6A 6A 6A †. ¶ÅÑíØ. Òë. hijjj.
10: 85 8B AD DF FF 85 9F 20 2B F1 08 A5 85 85 9B A5 Òäøfl ÖÜ. +Ò. •ÒÖöö.
20: 86 85 9C A9 E0 85 9A A5 82 C5 8A 85 8A 08 6A BD ÜÖöøtÖö. ¶ç~äÖä. jØ
30: 89 C0 90 01 E8 BD 8A C0 20 4C F3 28 F0 0A 28 A0 ä~é. ÈÖäæ. LÛ((t
40: 07 20 56 F4 88 D0 FA 08 A5 83 A6 81 20 04 F1 28 ..VÙà-..ÉØÀ..Ò((t
50: D0 17 A0 12 88 D0 FD E6 99 D0 F7 E6 9A 30 F3 20 -.t.à-”Èö~”ÈööÜ.
60: 2B F1 D0 05 A9 80 4C EA F0 A5 87 F0 76 C9 03 B0 +Ò. •CÄLÍ Apple. áAv...~
70: 72 6A B0 0B AD DF FF 29 7F 8D DF FF 20 C4 F2 A0 rj~. #fl) cf! .fÚ†
80: 7F 84 93 A6 81 20 B9 F1 90 22 20 AA F1 C6 93 10 Ñì¶À. Þöðì. (t
90: F2 C6 94 D0 53 A5 8F 30 E6 A5 8C 48 A9 60 20 25 Úðî-S. èøÈ. •åHØ. %
A0: F1 A9 00 20 04 F1 68 20 04 F1 90 D3 A4 99 C4 8C ðø...ðh.. ðè”Søfå
B0: F0 0E A5 8C 48 98 0A 20 25 F1 68 20 04 F1 90 CA Apple. •åHð. .%ðh.. ðè
C0: A5 9A 85 89 A5 98 C5 84 D0 C0 A5 87 4A 90 2A 20 •ÒÖö. ð~Ñ-~. áJè*.
D0: 48 F1 B0 B6 AD DF FF 29 7F 8D DF FF 20 0F F3 A6 Hðøø#fl) cf! ..Û¶
E0: 81 B0 A7 18 A9 00 90 03 A9 82 38 85 88 BD 88 C0 Á~ß. C. è. Øç8ØàØàæ
F0: 20 AA F1 A5 9F 8D DF FF 60 20 16 F2 90 E5 A9 81 .™ð. ðçfl` ..ÛðÅCÅ
100: 50 E8 D0 C4 0A 85 99 20 18 F1 20 3E F1 B5 85 85 PË-f. Òö. .ò. >ÒµÖÖ
110: 8C A5 99 95 85 20 00 F4 A0 03 98 20 4A F4 88 10 å. ðiÖ.. Ù†. ð. JÙà.
120: F9 46 8C 18 60 20 3E F1 95 85 60 A0 00 BD 8C C0 ^Få. ` >ÒiÖ. t. Øå~
130: 20 3D F1 48 68 DD 8C C0 D0 03 88 D0 F0 60 48 8A .=ÒHh. å~. -à. Apple. Hå
140: 4A 4A 4A 05 82 AA 68 60 A0 20 88 F0 6A BD 8C C0 JJJ. Çtmh` t. à. Apple. Øå~
150: 10 FB 49 D5 D0 F4 EA BD 8C C0 10 FB C9 AA D0 F2 . .”I. -ÙÍØå. . .™-U
160: A0 55 EA BD 8C C0 10 FB C9 AD D0 E6 EA BD 8C tUÍØå. . .#-ÈÍÍØå
170: C0 10 FB 99 02 03 AD EF FF 05 8B 10 37 88 10 EE å. . ð. .#Ø. . ð. . 7à. ð
180: C8 BD 8C C0 10 FB 99 00 02 AD EF FF 05 8B 10 24 »Øå. . ð. . #Ø. . ð. . \$
190: C0 E4 D0 EC C8 BD 8C C0 10 FB 99 00 02 C8 D0 F5 ð. . -Í. »Øå. . ð. . >-1
1A0: BD 8C C0 10 FB 85 96 20 01 F2 24 8B 10 04 24 8F Øå. . ðñ. . ÙS. . ð. . è
1B0: 10 01 58 60 20 AA F2 38 60 A0 FC 84 95 C8 D0 04 . . X. .™U8` t. Ñi. . -
1C0: E6 95 F0 F3 BD 8C C0 10 FB C9 D5 D0 F0 EA BD 8C Èi Apple. Øå. . . - -Apple. Øå
1D0: C0 10 FB C9 AA D0 F2 A0 03 BD 8C C0 10 FB C9 96 . . .™-U. Øå. . . ðñ
1E0: D0 E7 78 A9 00 85 89 BD 8C C0 10 FB 2A 85 95 BD -ÁxØ. Øå. Øå. . *ÖiØ
1F0: 8C C0 10 FB 25 95 99 97 00 45 89 88 10 E7 A8 D0 å. . %iöö. Eåå. Á®-
200: B6 BD 8C C0 10 FB C9 DE D0 AD EA BD 8C C0 10 FB ðØå. . . -fi-#ÍØå. .
210: C9 AA D0 A3 18 60 38 B8 BD 8D C0 BD 8E C0 30 F5 ...™-f. . 8ΠØç. Øé. Ø1
220: A9 FF 9D 8F C0 1D 8C C0 A0 04 EA 48 68 48 68 20 Ø. ð. . ð. . ð. . ð. .
230: BB F2 88 D0 F8 A9 D5 20 BA F2 A9 AA 20 BA F2 A9 ^Ùà-”Ø. . ð. . ð. .
240: AD 20 BA F2 A0 55 EA EA D0 08 AD EF FF 05 8B ≠. ð. . ð. . ð. . ð. .
250: 38 10 57 30 00 B9 02 03 9D 8D C0 BD 8C C0 88 10 8. W0. ð. . ð. . ð. .
260: EA 98 30 03 AD EF FF 05 8B 38 30 02 10 3C C8 B9 ð. . ð. . ð. . ð. .
270: 00 02 9D 8D C0 BD 8C C0 C0 E4 D0 E8 EA C8 EA EA ..ñç. Øå. . %. -ÈÍÍØí
280: 48 68 B9 00 02 9D 8D C0 BD 8C C0 A5 96 C8 D0 EE Hh. . . ð. . ð. . ð. .
290: F0 00 20 BB F2 48 68 B9 C0 F3 20 BD F2 C8 C0 04 Apple. . . ð. . ð. . ð. .

A80: C4 F9 B1 74 20 AE F9 A9 A0 4C 39 FC 4C F8 03 68 f~†t.Æ~©†L9,L~.h
A90: 68 20 D6 F9 6C 72 00 20 AE F9 A9 A1 20 39 FC 20 h.+~lr..Æ~©~.9..
AA0: 07 FD 4C 04 F9 A5 76 85 7A A5 77 85 7B 60 20 B8 .~L.~.vÖZ~wÖ{~.Π
AB0: FA 98 F0 1D C6 7D F0 45 CA D0 16 C9 BA D0 4B 85 ~Ø.Δ.)Apple~...J~KÖ
AC0: 7C A5 76 91 78 E6 78 D0 02 E6 79 60 A4 7D 88 B1 |~vëxÈx-.Èy~\$~à±
AD0: 7E 85 7C 60 A9 01 2C A9 02 85 87 A5 74 85 85 A5 ~Ø|~`©..©.Öá~tÖÖ~
AE0: 75 85 86 A6 7B A5 7A 78 20 79 F4 B0 AA E6 7A D0 uÖÜP{~.zx.yÙ~m~Èz~
AF0: 02 E6 7B E6 75 E6 75 20 9D F9 90 DF 60 A5 75 85 .~È{~Èu~Èu.~ù~éfl~.uÖ
B00: 77 20 CB F9 05 74 85 76 D0 06 4A B0 95 20 CB F9 w.À~.tÖV~.J~öi.À~
B10: A5 74 85 7A A5 75 85 7B 20 9D F9 B0 EE 20 75 FA ~tÖz~uÖ{~.ù~~ö.~u~
B20: 20 94 F9 B0 10 A5 74 25 69 D0 05 20 35 FB D0 ED .~i~.~t~i~.~5~.~i~
B30: 20 82 FA D0 EB A5 7A 85 74 A5 7B 85 75 20 87 FA .~C~.~i~zÖt~.©~ü~.~á~
B40: A0 00 B1 74 09 80 C9 A0 B0 02 A9 AE 20 39 FC 20 t~.~t~.~t~.~t~.~.~.~.~
B50: 8E F9 B0 06 A5 74 25 69 D0 E6 4C EF FC 38 AD 53 é~.~.~.~.~.~.~.~.~
B60: C0 B0 04 18 AD 52 C0 A5 68 09 40 B0 02 29 BF 85 ~.~.~.~.~.~.~.~.~
B70: 68 09 7F 29 A0 85 66 B0 02 A9 F0 85 67 A5 58 85 h.~)tÖf~.©~Ög~.XÖ
B80: 5C A5 5A 85 5D A5 5C 48 A5 5D 48 20 C5 FB 20 A2 \~.ZÖ]~.H~.~.~.~
B90: FB A5 58 85 5C 20 DD FB 90 F4 68 A8 68 85 5C 98 ~.~.~.~.~.~.~.~.~
BA0: B0 23 A5 5C 4C 89 FC C9 80 90 65 C9 8D D0 3A 20 ~.~.~.~.~.~.~.~.~
BB0: A2 FB 20 D7 FB 4C 16 FC A5 5D C6 5D C5 5A D0 02 ~.~.~.~.~.~.~.~.~
BC0: A5 5B 38 E9 01 85 5D A5 5D 10 4E 24 68 70 02 E6 ~.~.~.~.~.~.~.~.~
BD0: 5C E6 5C A5 5C C5 59 A5 58 90 5D 85 5C E6 5D A5 \~.~.~.~.~.~.~.~.~
BE0: 5D C5 5B 90 E2 A5 5A B0 DC C9 88 D0 5D 24 68 70]~[~.~.~.~.~.~.~.~.~
BF0: 02 C6 5C C6 5C 30 06 A5 5C C5 58 10 3B 20 B8 FB ~.~.~.~.~.~.~.~.~
C00: A5 59 85 5C D0 E7 C9 A0 90 9D 24 68 30 02 29 7F ~.~.~.~.~.~.~.~.~
C10: 20 9D FC 20 CB FB B0 43 60 08 48 4A 29 03 09 04 ~.~.~.~.~.~.~.~.~
C20: 85 5F 49 0C 85 61 68 29 18 90 02 69 7F 85 5E 0A Ö~.~.Ö~.~.Ö~.~.Ö~.~
C30: 0A 05 5E 85 5E 85 60 28 60 48 84 6D 86 6C 20 47 ..~.Ö~.~.Ö~.~.Ö~.~.Ö~.~
C40: FC A4 6D A6 6C 68 60 6C 6E 00 C9 87 D0 04 AE 40 ~.~.~.~.~.~.~.~.~
C50: C0 60 C9 8A D0 E2 20 DD FB 90 DD A5 5A 48 20 C5 ~.~.~.~.~.~.~.~.~
C60: FB A2 03 B5 5E 95 62 CA 10 F9 68 18 69 01 C5 5B ~.~.~.~.~.~.~.~.~
C70: B0 15 48 20 C5 FB A5 59 4A A8 88 30 E4 B1 5E 91 ~.~.~.~.~.~.~.~.~
C80: 62 B1 60 91 64 90 F3 A5 58 4A A8 B0 04 A5 66 91 bt~.~.~.~.~.~.~.~.~
C90: 5E A5 67 91 60 C8 98 0A C5 59 90 ED 60 24 68 70 ^~.gë~.»~.~.~.~.~.~
CA0: 0C 46 5C 06 5C 20 AD FC A5 67 91 60 60 48 A5 5C ~.~.~.~.~.~.~.~.~
CB0: 4A A8 68 B0 F5 91 5E 60 B1 7E 20 39 FC C9 88 F0 J~.~.~.~.~.~.~.~.~
CC0: 1D C9 98 F0 08 E6 80 A5 80 C9 50 D0 17 A9 DC 20 ...~.~.~.~.~.~.~.~.~
CD0: 39 FC 20 EF FC A5 6B 20 39 FC A0 01 84 80 A4 80 9.~.~.~.~.~.~.~.~
CE0: F0 F3 C6 80 20 60 FD A4 80 91 7E C9 8D D0 C9 2C ~.~.~.~.~.~.~.~.~
CF0: 00 C0 10 13 20 2E FD C9 A0 F0 07 C9 89 D0 08 4C .~.~.~.~.~.~.~.~.~
D00: 9F FA AD 00 C0 10 FB A9 8D 4C 39 FC 6C 70 00 A9 ü~.~.~.~.~.~.~.~.~
D10: 7F 85 63 20 88 FD 48 20 35 FD B0 08 A5 69 20 9D Öc.~.~.~.~.~.~.~.~.~
D20: FC 20 35 FD 68 08 48 20 9D FC 68 28 90 E8 AD 00 .~.~.~.~.~.~.~.~.~
D30: C0 2C 10 C0 60 E6 62 D0 09 E6 63 A9 7F 18 25 63 .~.~.~.~.~.~.~.~.~
D40: F0 05 0E 00 C0 90 EE 60 20 77 FD A5 68 29 80 49 ~.~.~.~.~.~.~.~.~
D50: AB 85 69 20 0C FD A0 08 D9 F0 FF F0 EB 88 10 F8 ~.~.~.~.~.~.~.~.~
D60: A9 80 25 68 85 69 20 0C FD C9 9B F0 DE C9 95 D0 ~.~.~.~.~.~.~.~.~
D70: D6 20 88 FD 09 80 60 A9 FB 48 B9 7F FD 48 60 A1 +.~.~.~.~.~.~.~.~
D80: 84 7C 62 5C EC CA DC B7 A5 5C 4A A8 24 68 50 05 Ñ|b|~.~.~.~.~.~.~.~.~
D90: 90 03 B1 60 60 B1 5E 60 A9 03 8D D0 FF D8 A2 03 ê.~.~.~.~.~.~.~.~
DA0: 86 7F BD BC FF 9D CA FF BD B4 FF 95 6E BD B8 FF Ü~.~.~.~.~.~.~.~.~
DB0: 95 58 CA 10 ED 85 82 A9 A0 85 7E A9 60 85 81 A9 iX.~.~.~.~.~.~.~.~
DC0: FF 85 68 20 63 FB A9 78 85 A0 A9 08 85 A1 A9 F0 ~.~.~.~.~.~.~.~.~
DD0: 85 A4 A9 00 AA 95 B4 E8 E0 20 D0 F9 A9 05 18 08 ÖS.~.~.~.~.~.~.~.~
DE0: 48 86 A2 A0 07 A6 A2 8A 91 A0 E8 88 30 06 C0 03 HÜç~.~.~.~.~.~.~.~
DF0: D0 F5 F0 F1 20 99 FE B0 08 C9 0A D0 E6 A2 24 D0 ~.~.~.~.~.~.~.~.~
E00: E0 68 28 A2 17 A0 05 36 B8 0A D0 0E 84 A2 C6 A4 ~.~.~.~.~.~.~.~.~
E10: F0 16 A4 A4 B9 C4 FE 2A A4 A2 88 D0 EA CA 10 E5 ~.~.~.~.~.~.~.~.~
E20: 08 48 20 28 FE 4C 01 FE A2 1F A0 00 B5 B4 0A 29 ~.~.~.~.~.~.~.~.~
E30: 3E 91 A0 CA C8 C0 08 D0 F3 20 99 FE C9 08 F0 04 ~.~.~.~.~.~.~.~.~
E40: 8A 10 E7 60 A9 01 85 A2 A9 60 2C DB C0 20 AE FE ~.~.~.~.~.~.~.~.~
E50: A9 20 20 AE FE 2C DA C0 20 88 FE C6 A2 10 16 A9 ~.~.~.~.~.~.~.~.~
E60: 08 85 A1 A0 07 B1 A0 18 69 08 91 A0 88 10 F6 20 ~.~.~.~.~.~.~.~.~

E70: 99 FE 90 EF 60 A0 03 A9 7F 99 FC 05 99 FC 07 88 ö_êô`†.© ô..ô..à
 E80: 10 F7 A9 08 85 A1 D0 C0 A0 07 B1 A0 49 20 91 A0 .~©.Ö°-‡†.±†I.ë†
 E90: 88 10 F7 20 99 FE 90 F0 60 A5 A0 49 80 85 A0 30 à.~.ô.êapple`•+IAÖ†0
 EA0: 02 E6 A1 A5 A1 C9 0C D0 04 A9 04 85 A1 60 85 A3 .È°.°...-.©.Ö°`Ö£
 EB0: AD EC FF 29 3F 05 A3 8D EC FF A9 08 8D ED FF 2C ≠i~)?..fcí`©.çí~,
 EC0: ED FF F0 FB 60 F0 01 82 18 40 84 81 2F 58 44 81 ï`apple`•apple.ç.©ÑÄ/XDÄ
 ED0: 29 02 1E 01 91 7C 1F 49 30 8A 08 43 14 31 2A 22)...ë|.I0ä.C.1*"
 EE0: 13 E3 F7 C4 91 48 A2 DA 24 C6 4A 62 8C 24 C6 F8 .."~fëH¢/\$ΔJbå\$Δ-
 EF0: 63 8C C1 46 17 52 8A AF 16 14 E3 33 31 C6 F8 DC cå;F.RäØ..„31Δ-
 F00: 73 3F 46 17 62 8C 21 E6 18 6A 8D 61 CF 18 62 74 s?F.bå!È.jçæ.bt
 F10: D1 B9 18 49 4C 91 C0 F3 09 2C 91 C0 14 1D 8C EF -π.ILë;Û.,ë.å..åÔ
 F20: 07 17 43 88 31 84 1E DF 0B 31 84 F8 FE 77 3E 3E ..Cå1Ñ.fl.1Ñ-,w>>
 F30: 17 62 8C FD C7 50 E3 0B 51 C5 E8 C8 73 18 0C 42 .bå"«P„.Q≈È»s..B
 F40: 3E 01 02 20 42 3E 41 18 8C 08 00 70 EE 00 11 11 >...B>A.å..pô...
 F50: 21 11 02 E0 3C 21 31 02 E0 1C 00 C8 B9 80 62 14 !...‡<!1.‡..»πÄb.
 F60: 1F 46 A2 DE 43 2C 04 88 BE FF CE 7D 37 49 88 95 .F¢fiC,.åæ`©}7Iäi
 F70: 18 98 09 62 D1 44 E8 88 FB 02 90 40 00 10 E0 03 .ò.b-DÈà.ê@..‡.
 F80: 02 00 40 00 00 08 00 00 28 10 42 44 25 82 B8 2F ..@....(.BD%çΠ/
 F90: 48 25 44 10 82 02 00 2F 5A 40 45 02 8E 64 50 90 H%D.ç../Z@E.édPé
 FA0: 01 3E 26 42 80 21 80 00 05 00 F8 80 00 05 08 F8 .>&BÄ!Ä...-Ä...-
 FB0: 80 28 05 88 06 FC 0F FD 00 50 00 18 4C 86 F6 40 Ä(.å...~.P..LÜ^@..
 FC0: 00 00 00 00 00 00 00 00 00 00 4C 86 F6 40 00 00 ..~.~.~.~.LÜ^@..
 FD0: 03 F7 FF FF D3 F3 FF FE 9D 4E FF 00 00 00 80 F7 ..~.~.~.~.~.~.~.~.
 FE0: C0 70 3F 0F C4 38 FE FE 77 77 FF 00 20 18 80 70 ïp?.f8..ww`...Äp
 FF0: CC D0 D3 B4 B8 88 95 8A 8B 00 CA FF EE F4 CD FF Ä-”¥Iaiää. ~ÓÙÖ~
 ###


differs from my ROM listing —
 copyright notice is gone and
 some maybe code is here.